

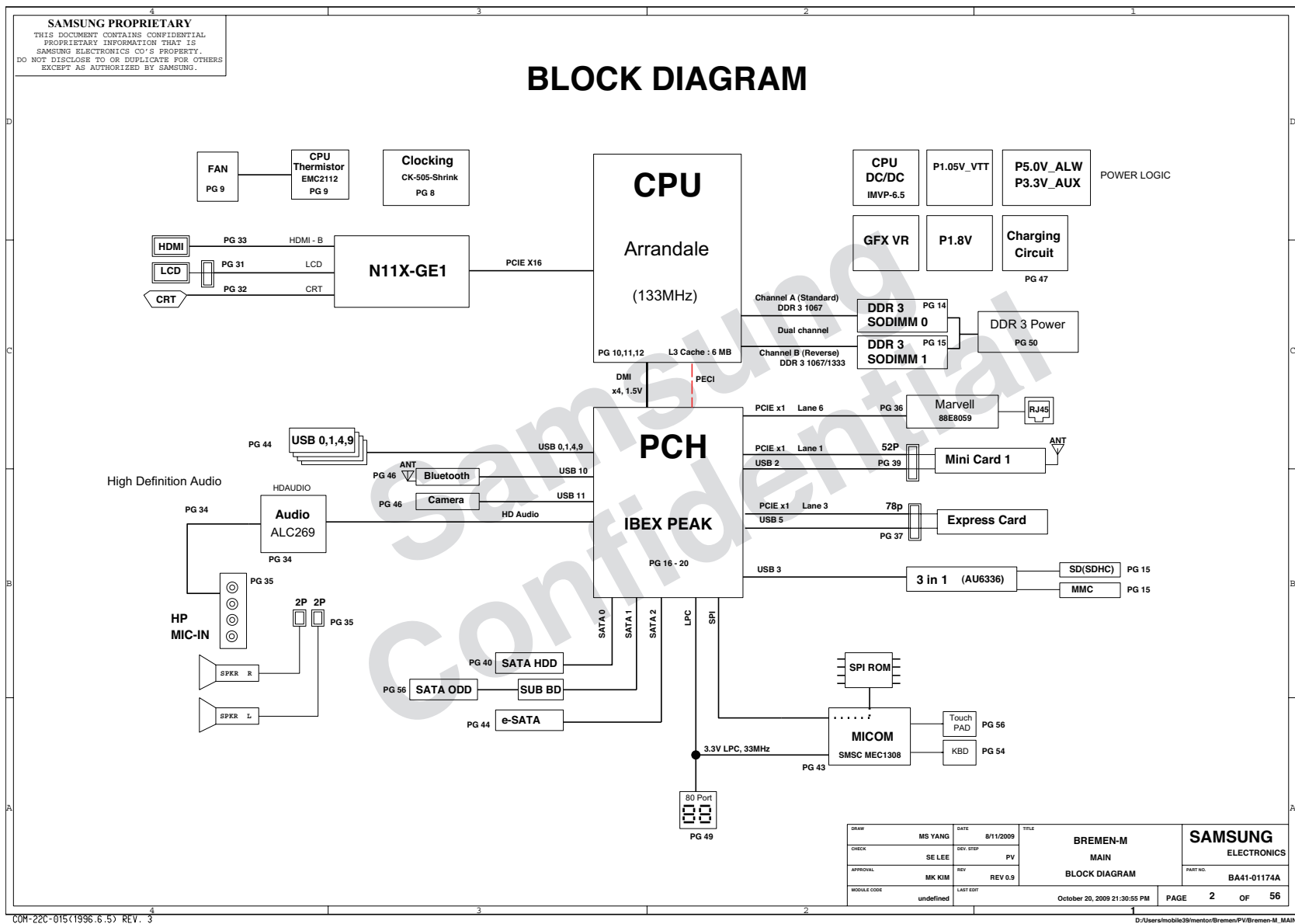
8. Block Diagram and Schematic

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<h1 style="margin: 0;">BREMEN-M 15"/17"</h1>																																			
<p>CPU : Intel Arrandale</p> <p>Chip Set : Intel Ixex Peak(PM55)</p> <p>GFX : N11P/ M-GE1(40nm)</p> <p>Remarks : Calpella Platform</p>																																			
<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="padding: 5px;">Model Name</td> <td style="padding: 5px;">: BREMEN-M</td> </tr> <tr> <td style="padding: 5px;">PBA Name</td> <td style="padding: 5px;">: MAIN</td> </tr> <tr> <td style="padding: 5px;">PCB Code</td> <td style="padding: 5px;">: BA41-01174A</td> </tr> <tr> <td style="padding: 5px;">Dev. Step</td> <td style="padding: 5px;">: PV</td> </tr> <tr> <td style="padding: 5px;">Revision</td> <td style="padding: 5px;">: 0.9</td> </tr> <tr> <td style="padding: 5px;">T.R. Date</td> <td style="padding: 5px;">: 2009.10.22</td> </tr> </table>			Model Name	: BREMEN-M	PBA Name	: MAIN	PCB Code	: BA41-01174A	Dev. Step	: PV	Revision	: 0.9	T.R. Date	: 2009.10.22																					
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<p>Sheet 1. Cover</p> <p>Sheet 2-6. Diagram (Block/Power) & Annotations</p> <p>Sheet 7. Clock Generator -CK505 Shrink Version</p> <p>Sheet 8. Thermal Sensor & FAN</p> <p>Sheet 9-12. Arrandale CPU</p> <p>Sheet 13. DDR3 SODIMM A</p> <p>Sheet 14. DDR3 SODIMM B</p> <p>Sheet 15-19. IBEX PEAK</p> <p>Sheet 20-24. GFx_External_N11X</p> <p>Sheet 25-28. Graphics_Memory_gDDR3 1g bit</p> <p>Sheet 29. Graphic Strap</p> <p>Sheet 30-32. Graphic Interface</p> <p>Sheet 33-34. High Definition Audio (ALC269Q-GR)</p> <p>Sheet 35. LAN_Marvell_88E8059</p> <p>Sheet 36. Express_Card</p> <p>Sheet 37. MultiCard_AU6336 (3 in 1)</p> <p>Sheet 38. PCIE_Minicard_Slot (Half type only)</p> <p>Sheet 39. SATA_I/F_CONN</p> <p>Sheet 40. MICOM_MEC1308-NU</p> <p>Sheet 41. Micom Glue Logic</p> <p>Sheet 42. LED_Switch</p> <p>Sheet 43. USB Port</p> <p>Sheet 44. Touchpad LED</p> <p>Sheet 45. USB Interface</p> <p>Sheet 46. Charger</p> <p>Sheet 47. PWR_3V_5V</p> <p>Sheet 48. PWR_Calpella</p> <p>Sheet 49. PWR_Memory</p> <p>Sheet 50. PWR_CPU_VRM</p> <p>Sheet 51. PWR_GFX_Ext</p> <p>Sheet 52. PWR_MV_Switched</p> <p>Sheet 53. PWR_MV_DisCharger</p> <p>Sheet 54. 2 USB Port & Power S/W SUB B'D</p> <p>Sheet 55. SATA ODD SUB B'D Battery IF B'D</p> <p>Sheet 56. EMC Capacitor & ICT Port</p>		<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 33%;">DRAW</td> <td style="width: 33%;">DATE</td> <td style="width: 33%;">TITLE</td> </tr> <tr> <td>MS YANG</td> <td>8/11/2009</td> <td>BREMEN-M</td> </tr> <tr> <td>CHECK</td> <td>DEV. STEP</td> <td>MAIN</td> </tr> <tr> <td>SE LEE</td> <td>PV</td> <td>COVER</td> </tr> <tr> <td>APPROVAL</td> <td>REV</td> <td></td> </tr> <tr> <td>MK KIM</td> <td>REV 0.9</td> <td></td> </tr> <tr> <td colspan="2">MODULE CODE</td> <td>PART NO.</td> </tr> <tr> <td colspan="2">LAST EDIT</td> <td>BA41-01174A</td> </tr> <tr> <td colspan="2">October 20, 2009 21:30:55 PM</td> <td></td> </tr> <tr> <td colspan="2">PAGE</td> <td>OF</td> </tr> <tr> <td colspan="2">1</td> <td>56</td> </tr> </table>	DRAW	DATE	TITLE	MS YANG	8/11/2009	BREMEN-M	CHECK	DEV. STEP	MAIN	SE LEE	PV	COVER	APPROVAL	REV		MK KIM	REV 0.9		MODULE CODE		PART NO.	LAST EDIT		BA41-01174A	October 20, 2009 21:30:55 PM			PAGE		OF	1		56
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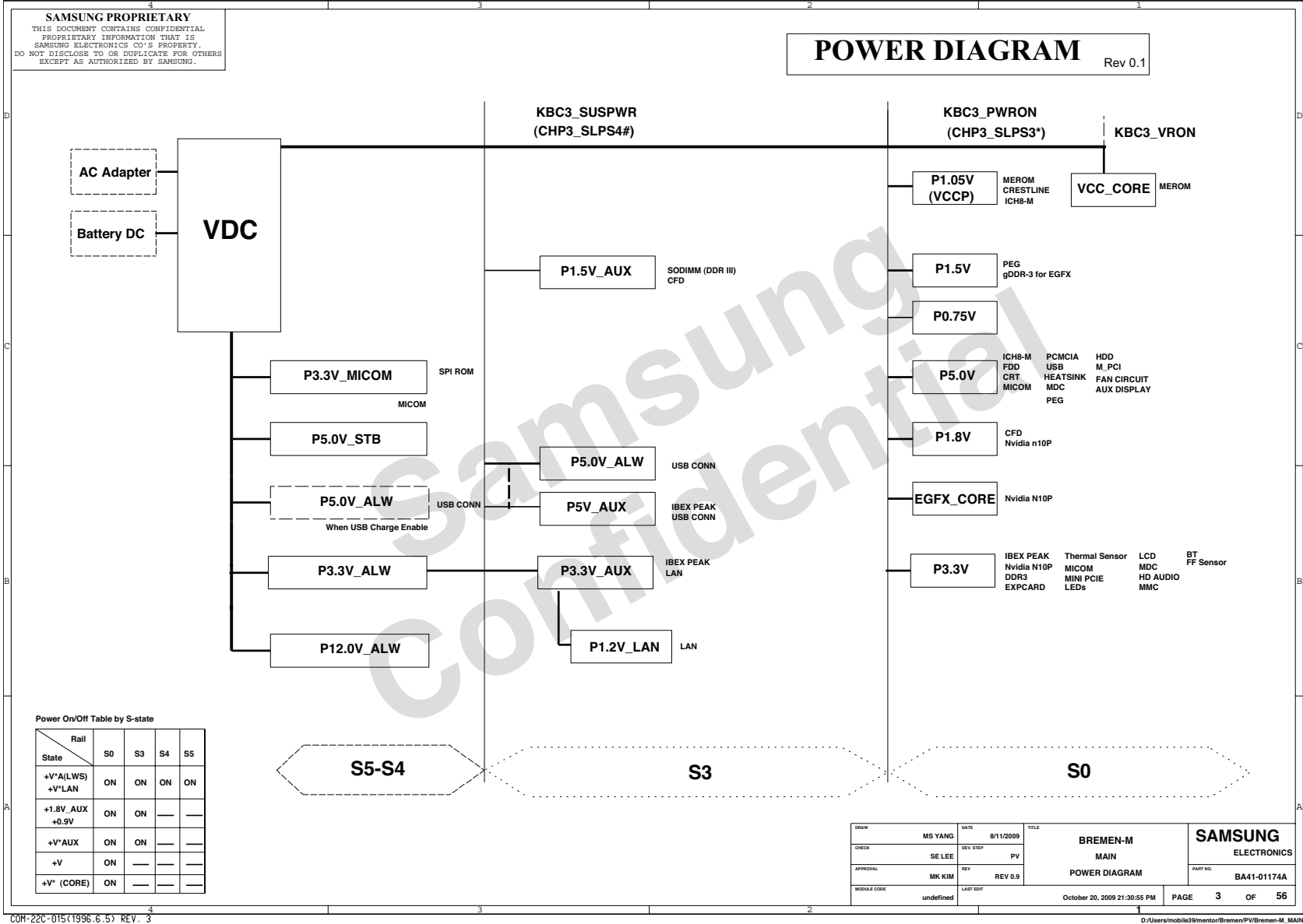
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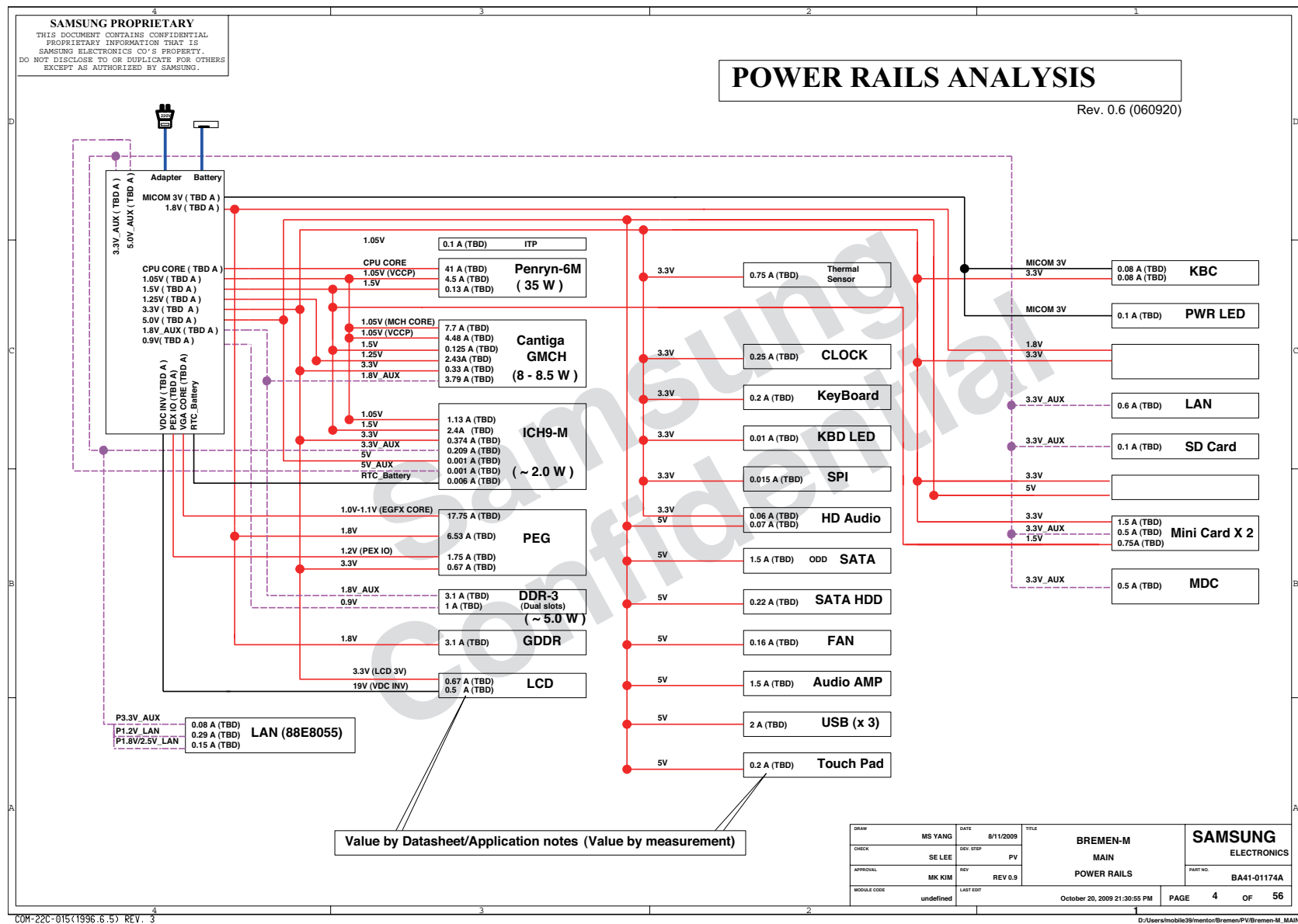
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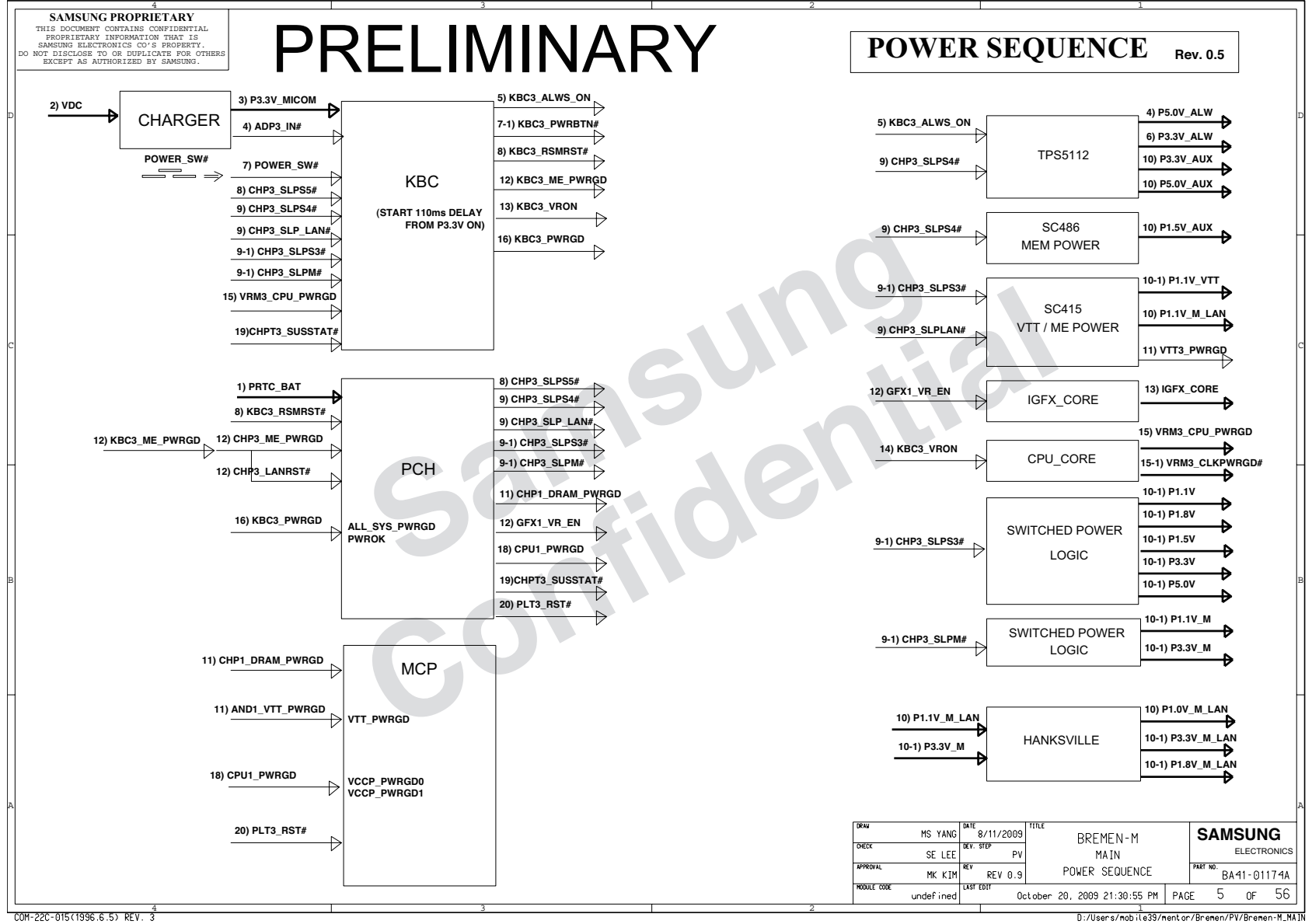
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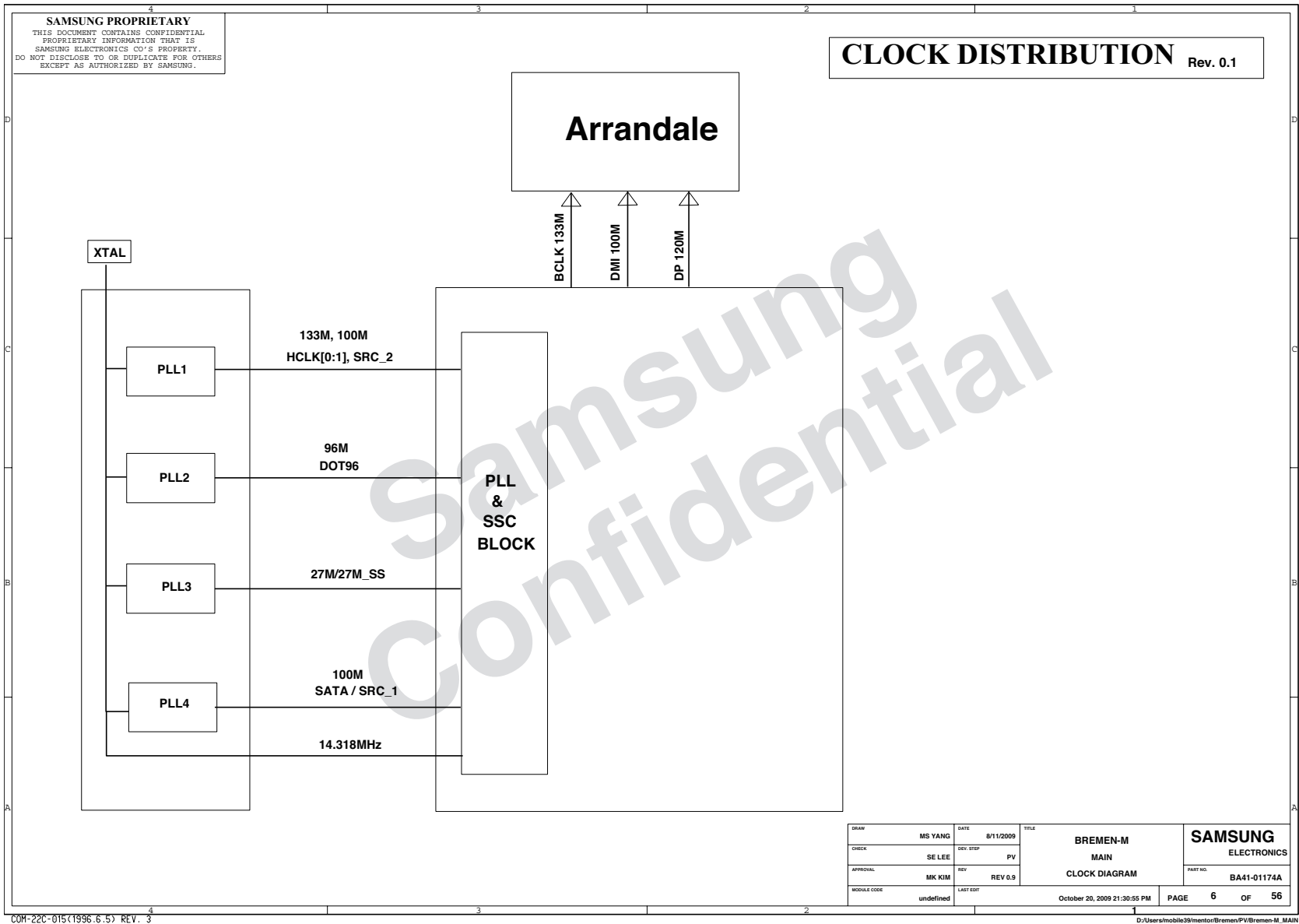
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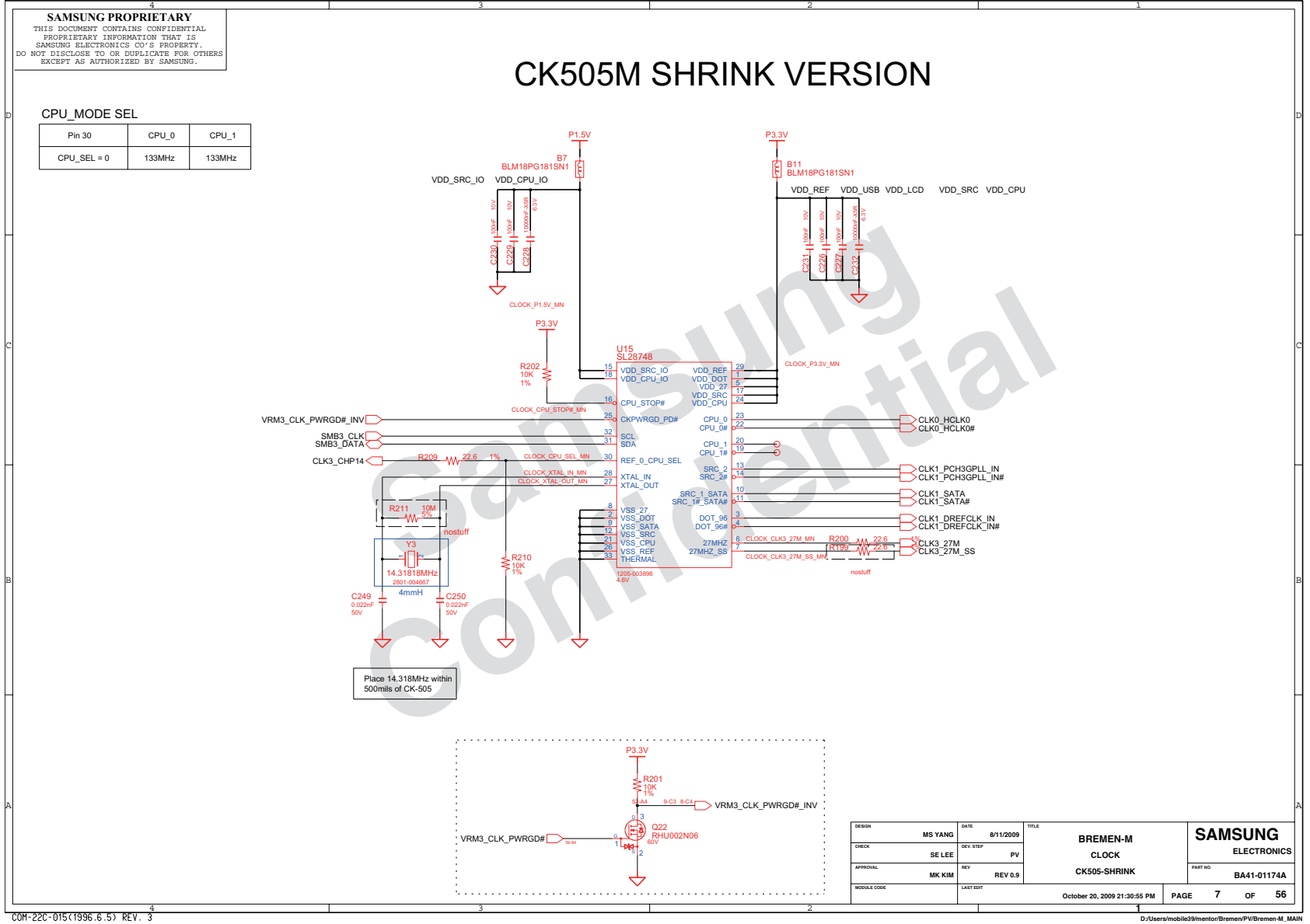
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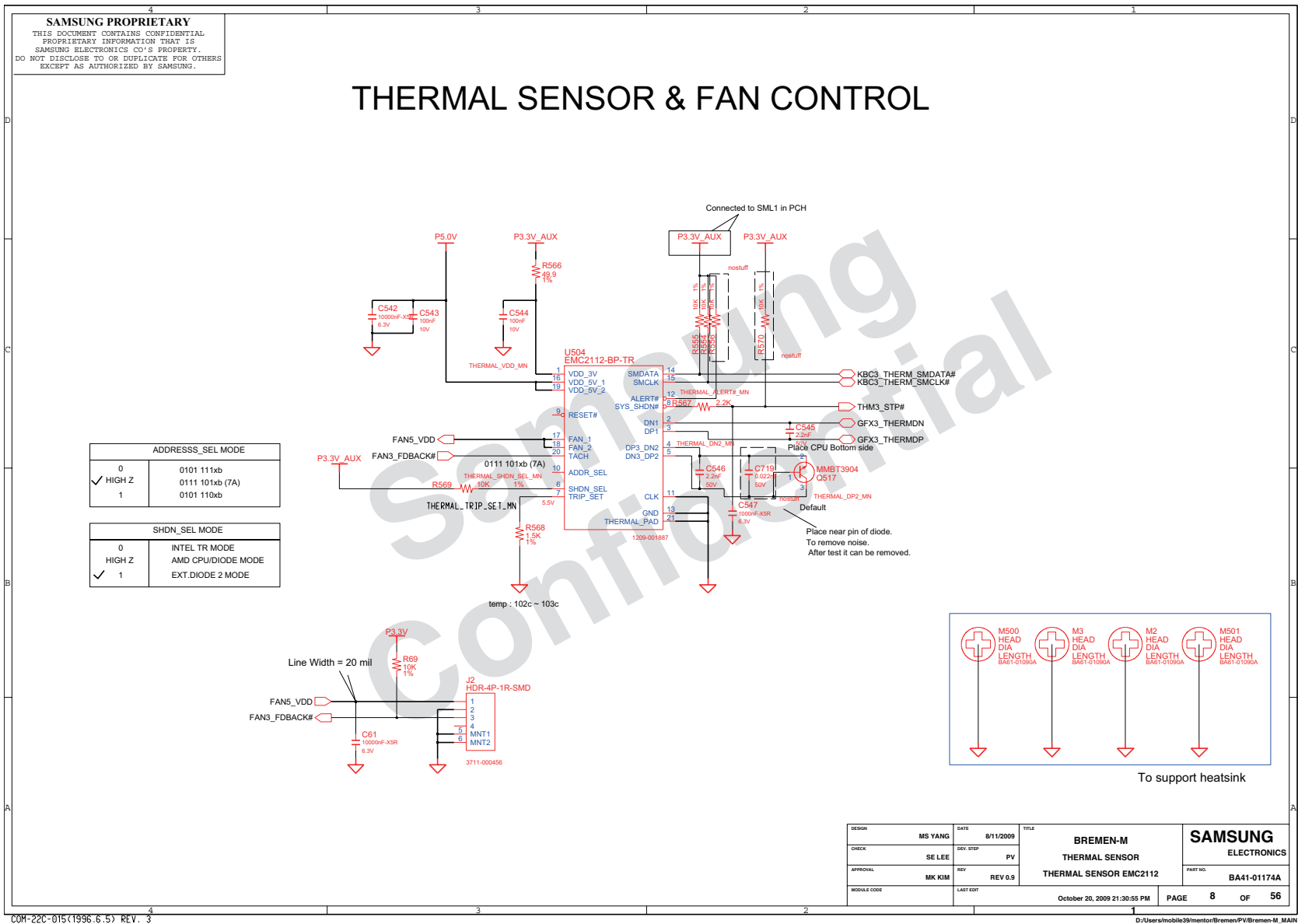
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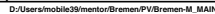
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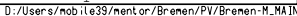


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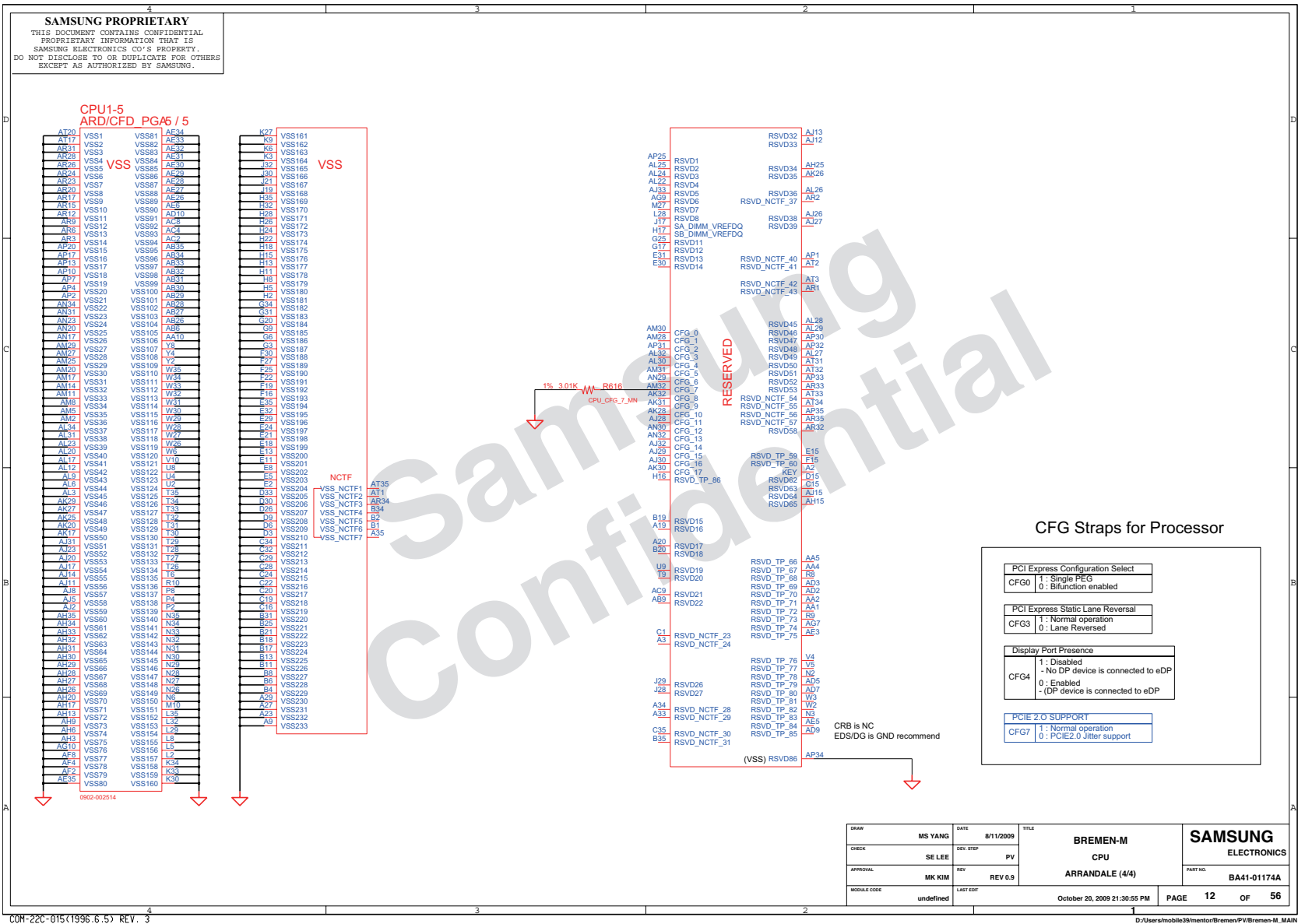


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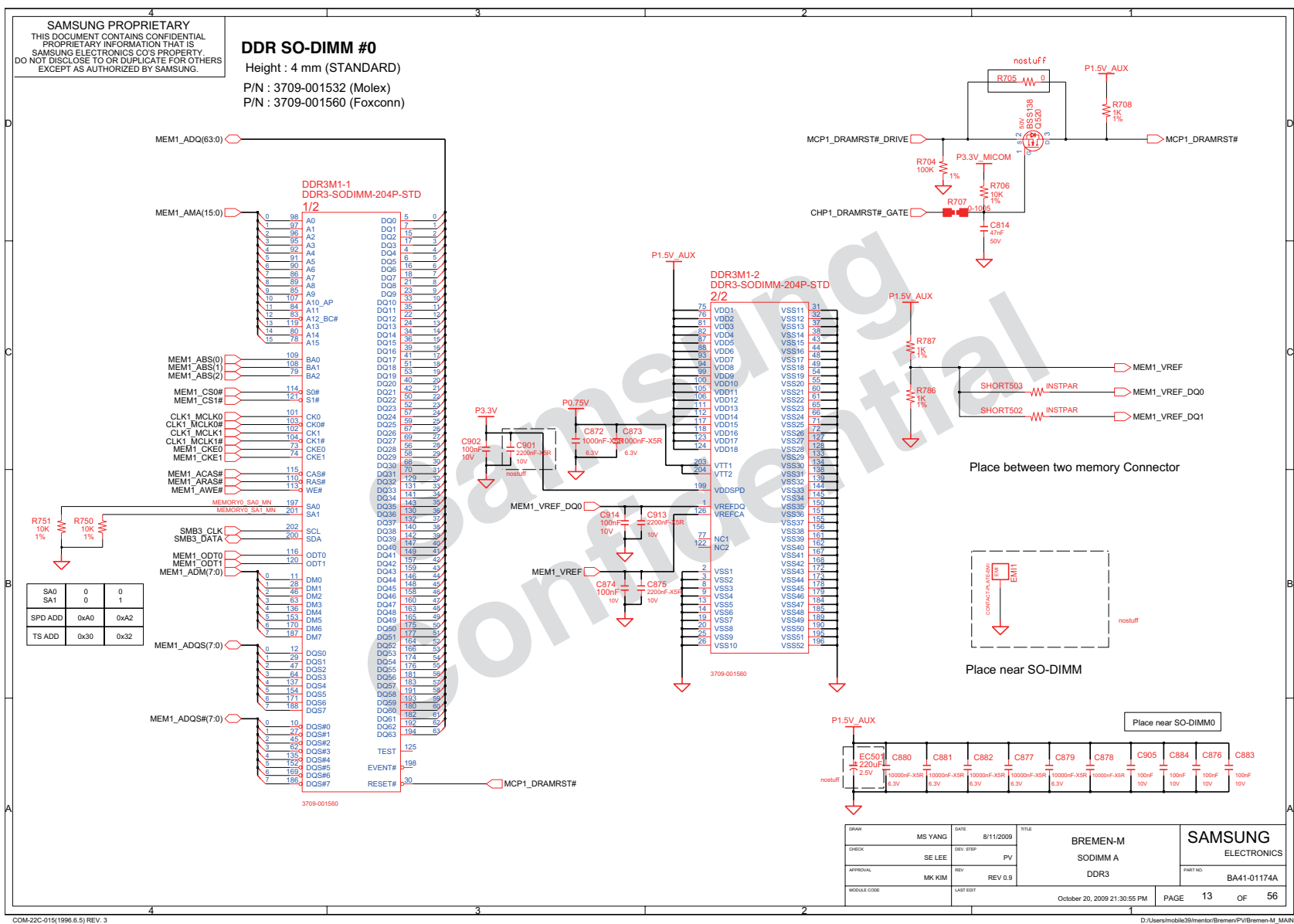


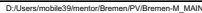


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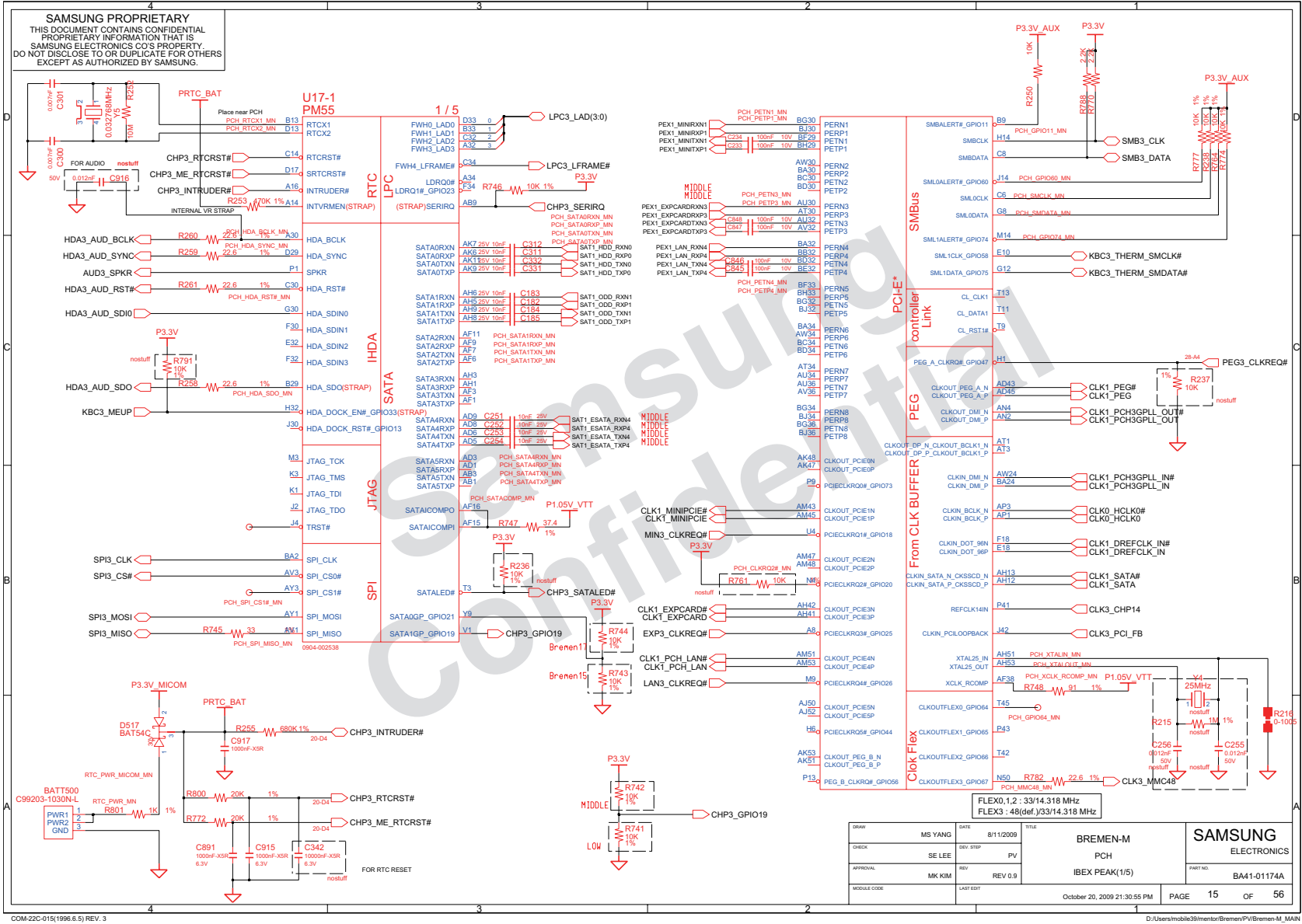


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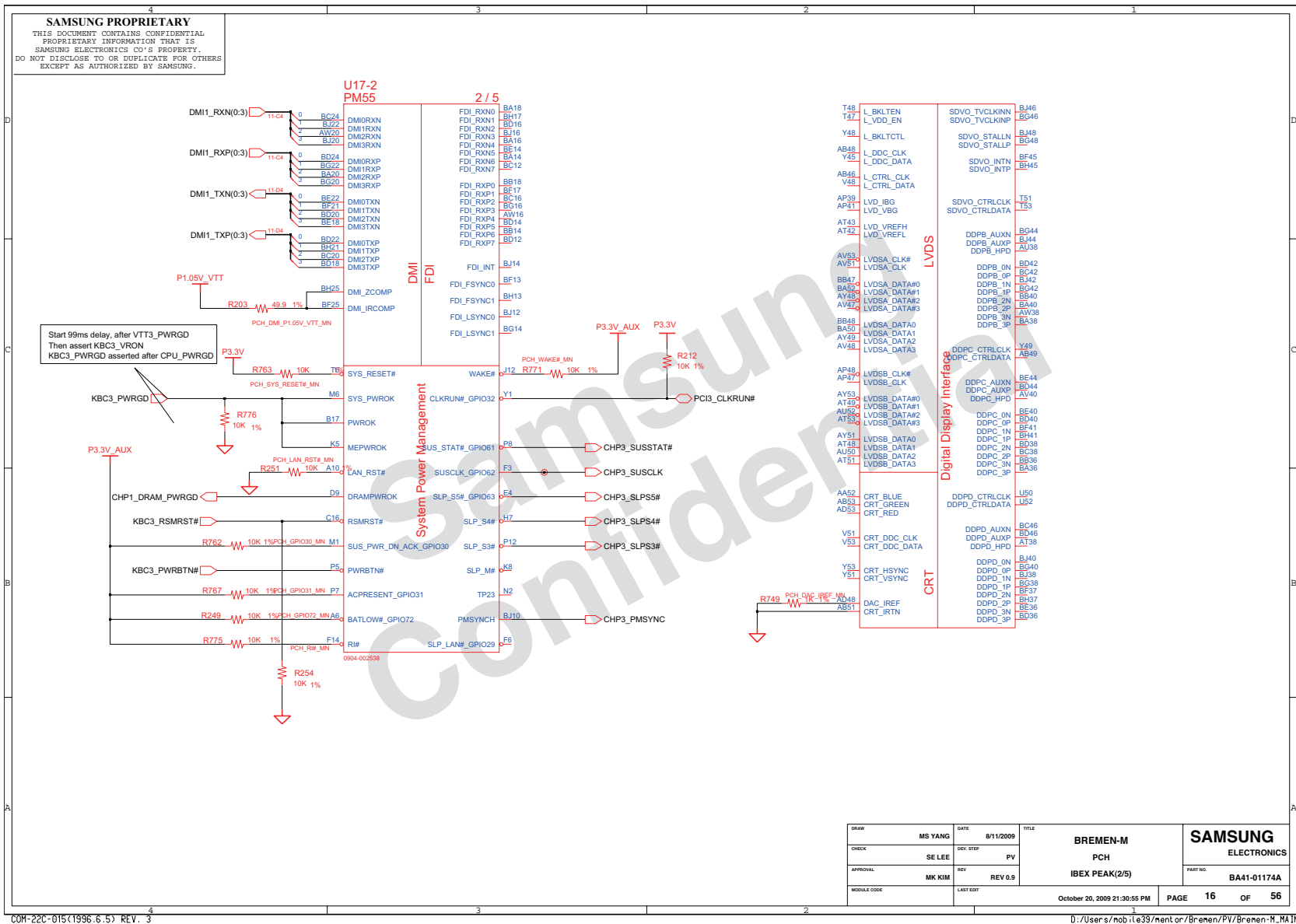




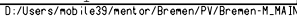
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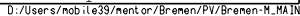


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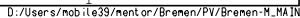


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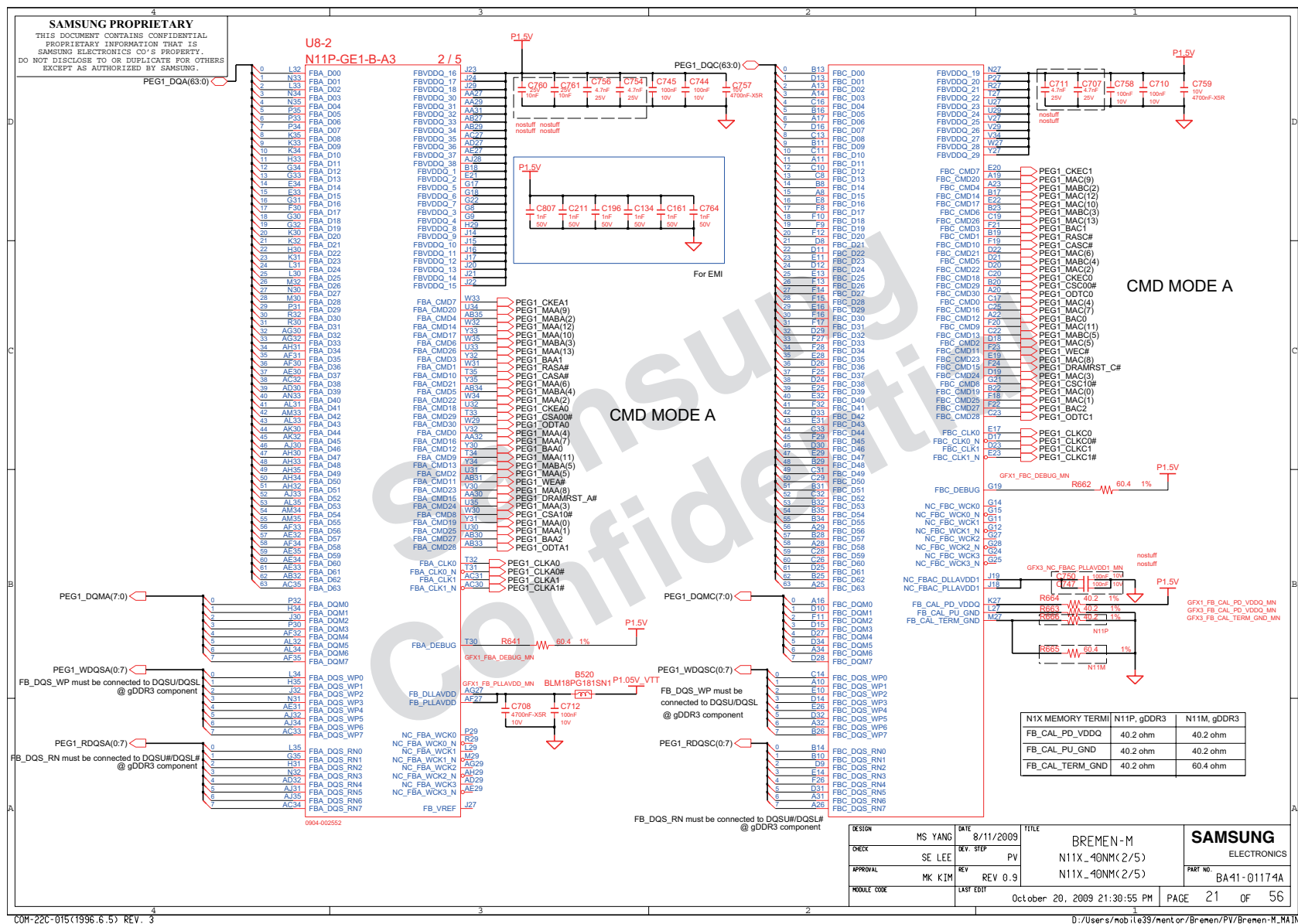




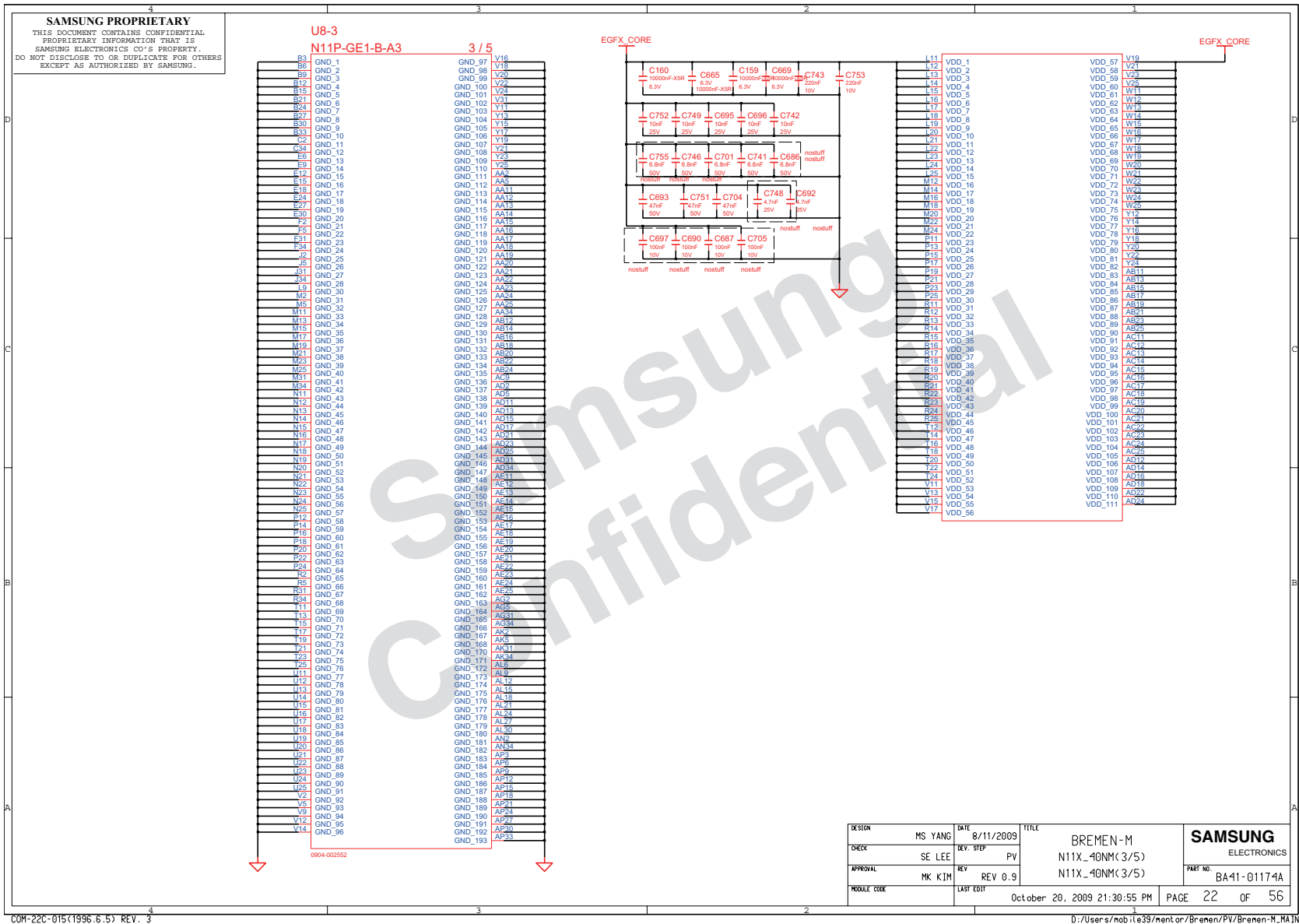




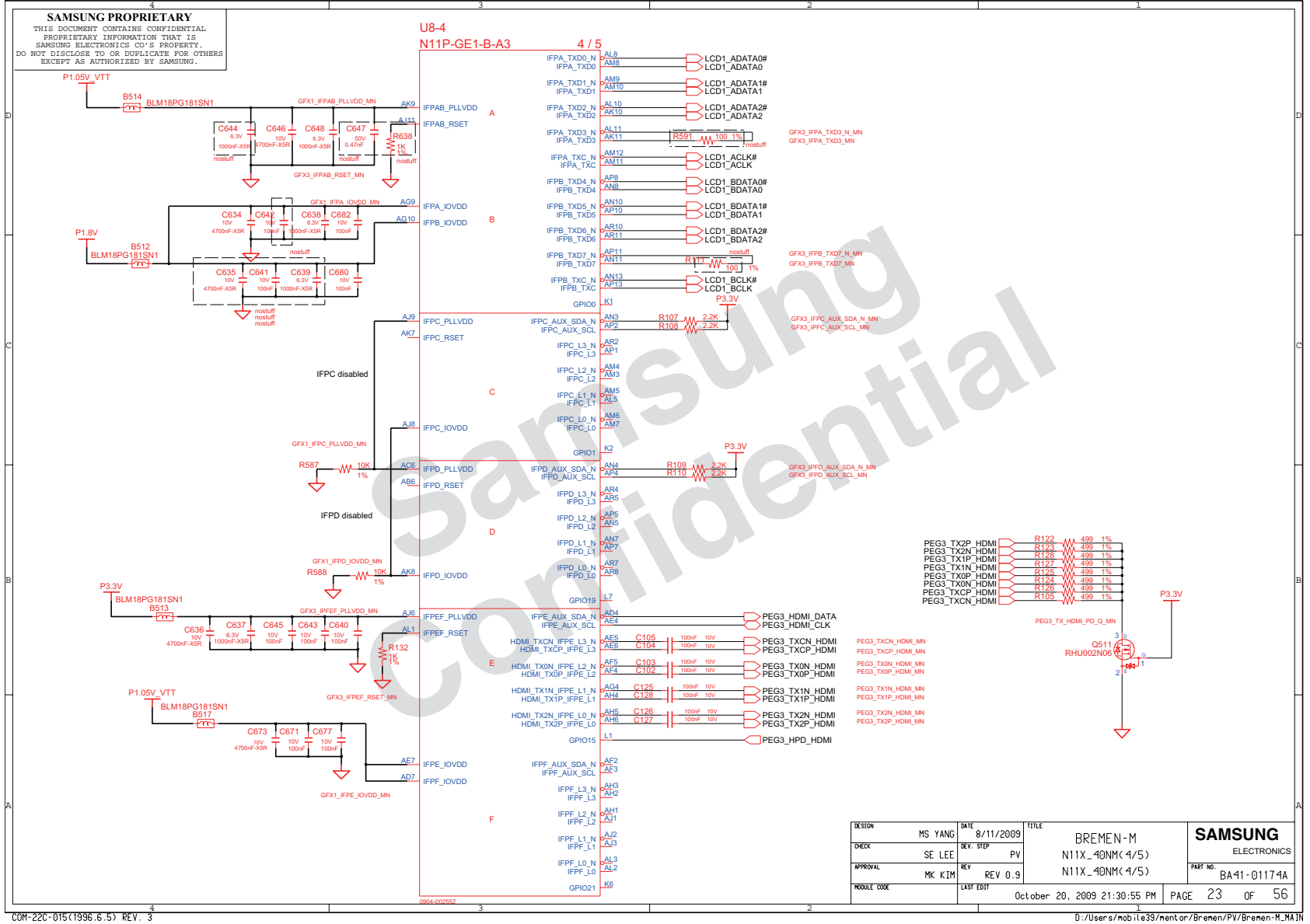
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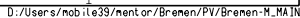


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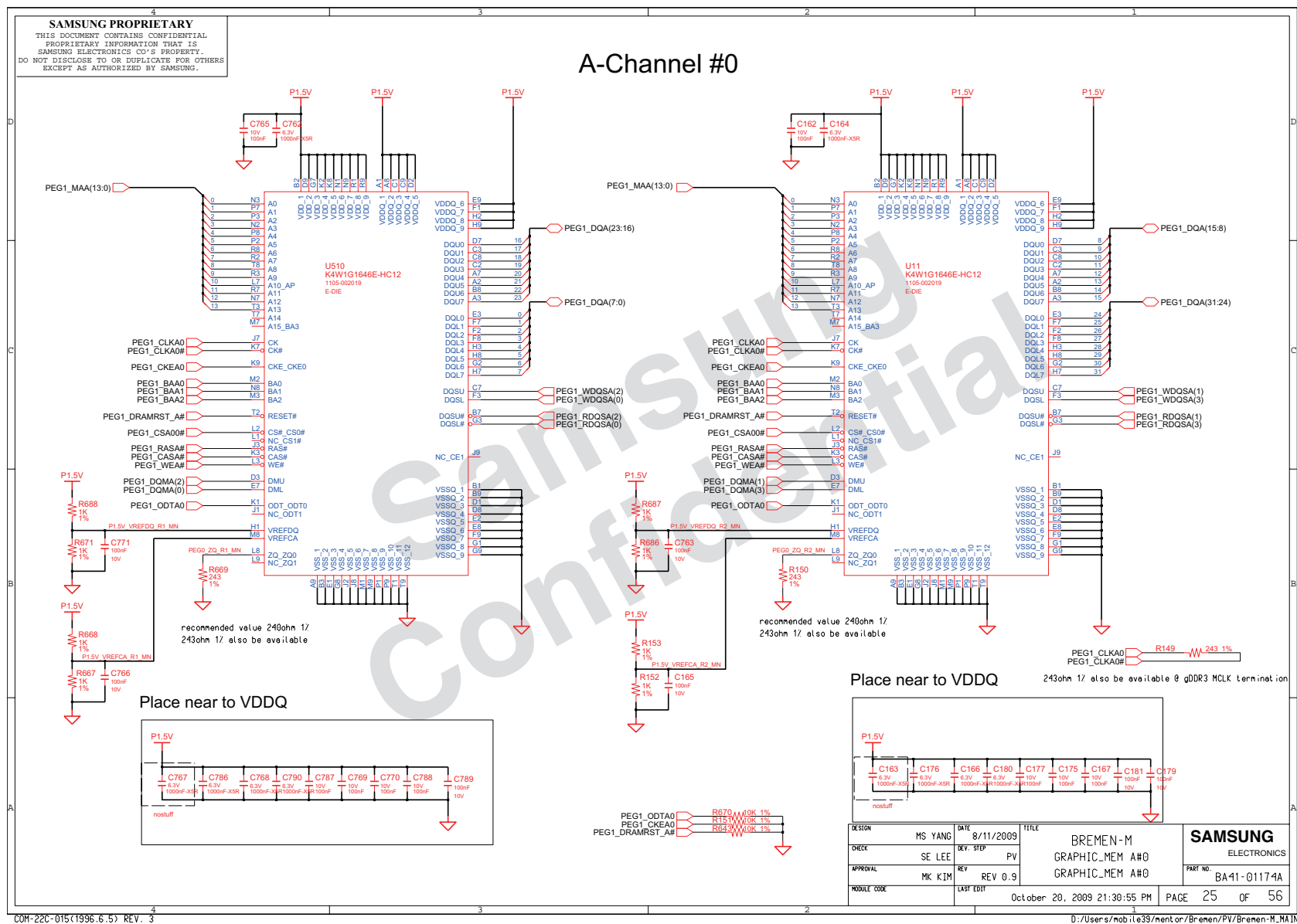


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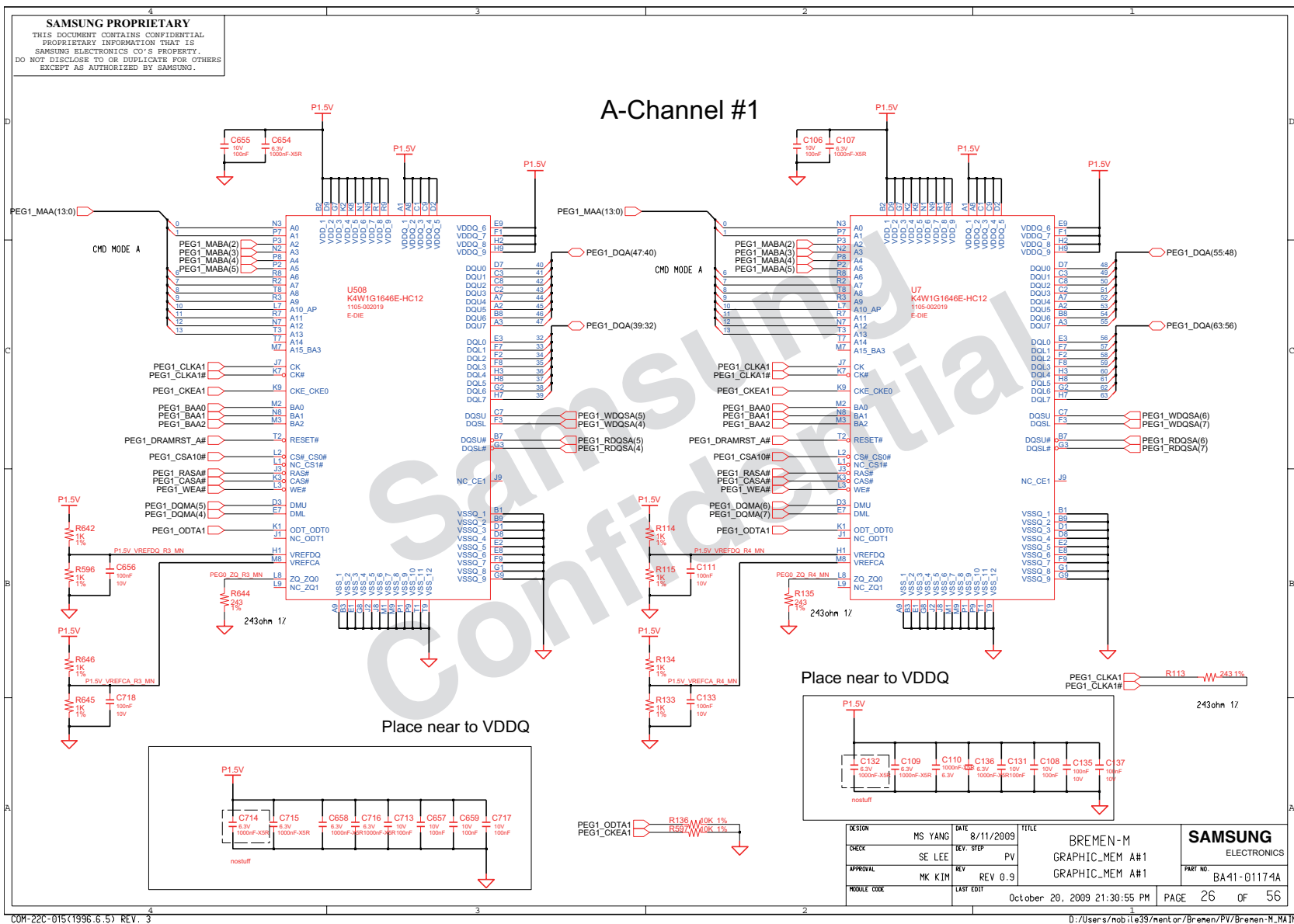




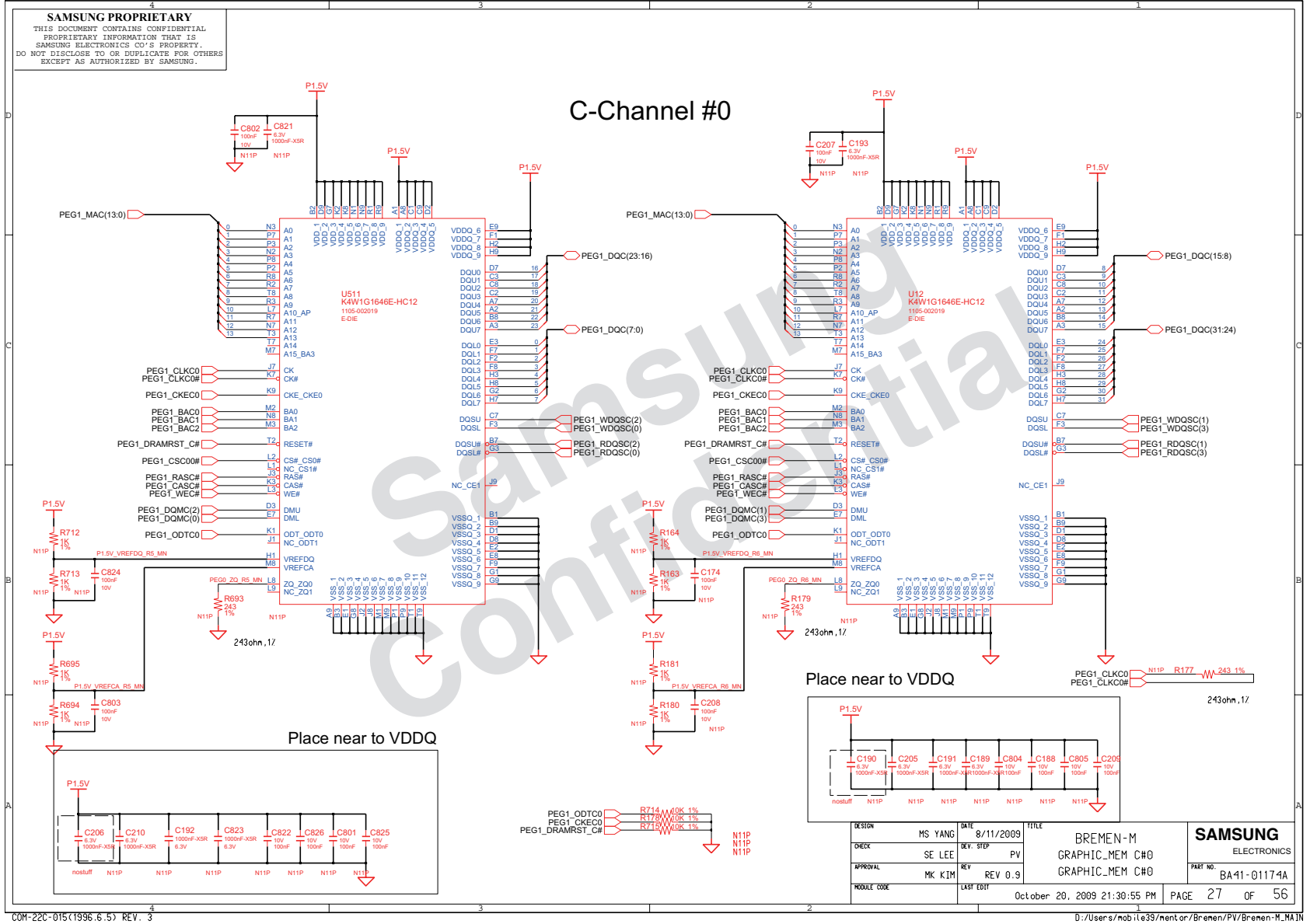
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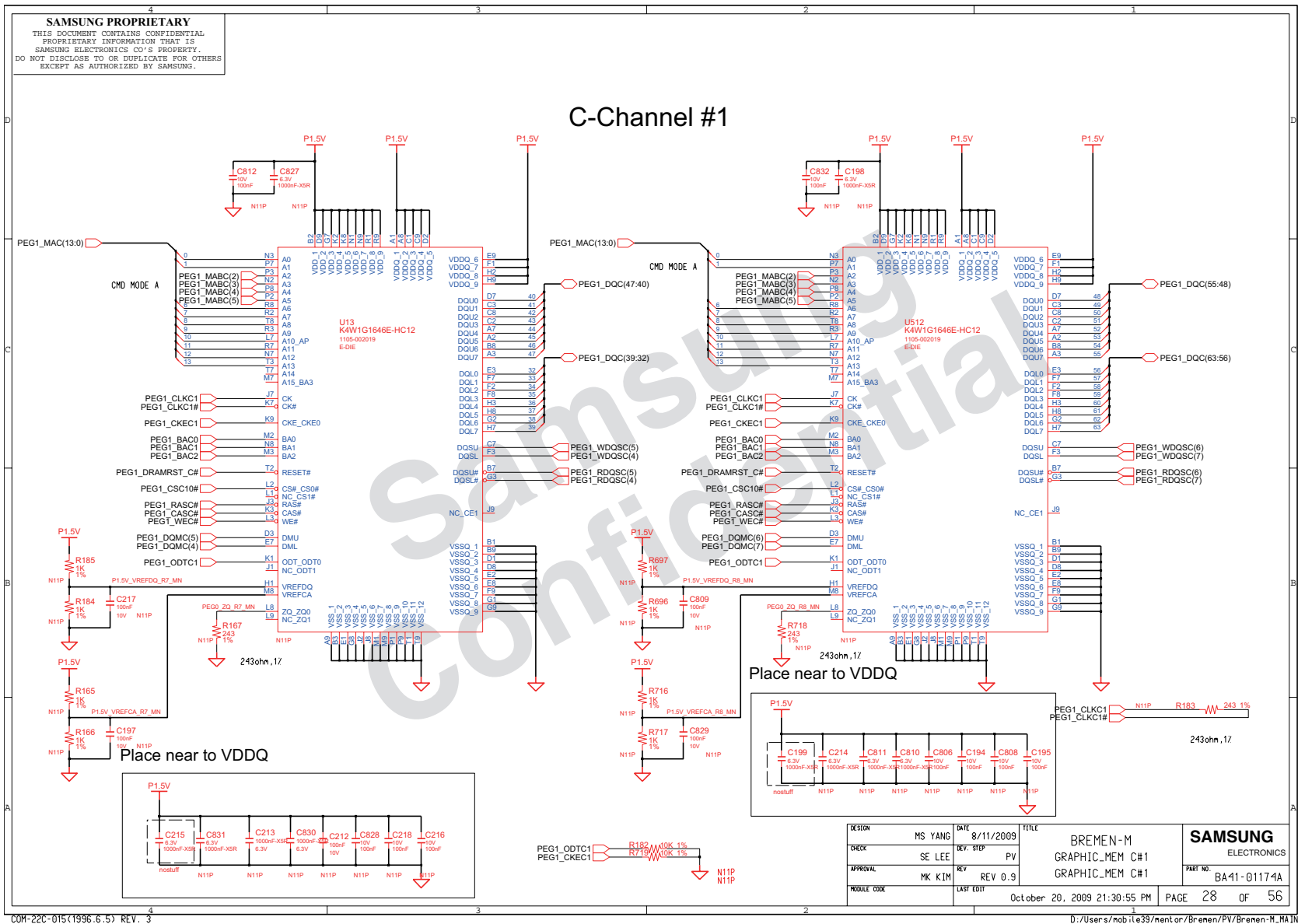
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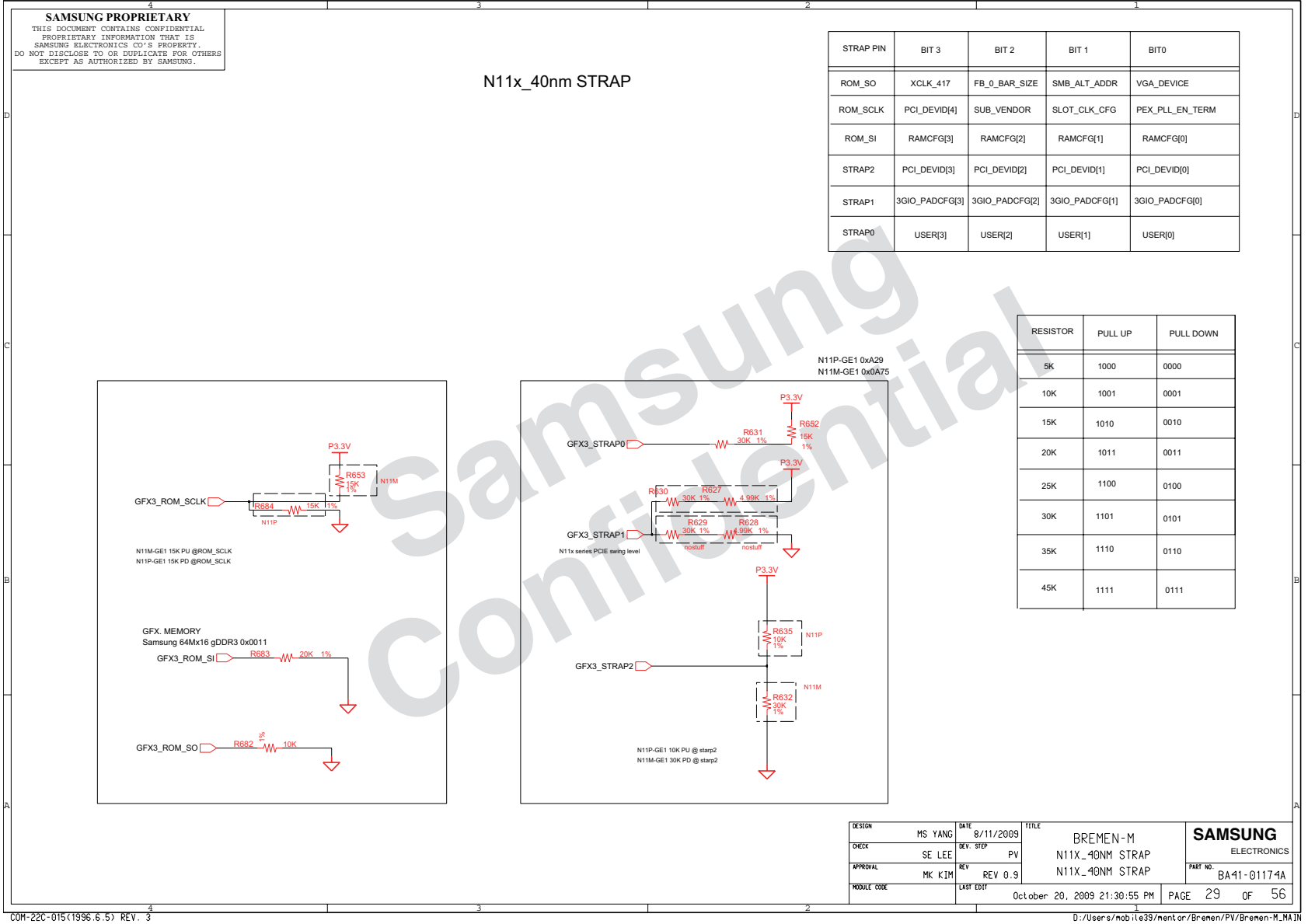
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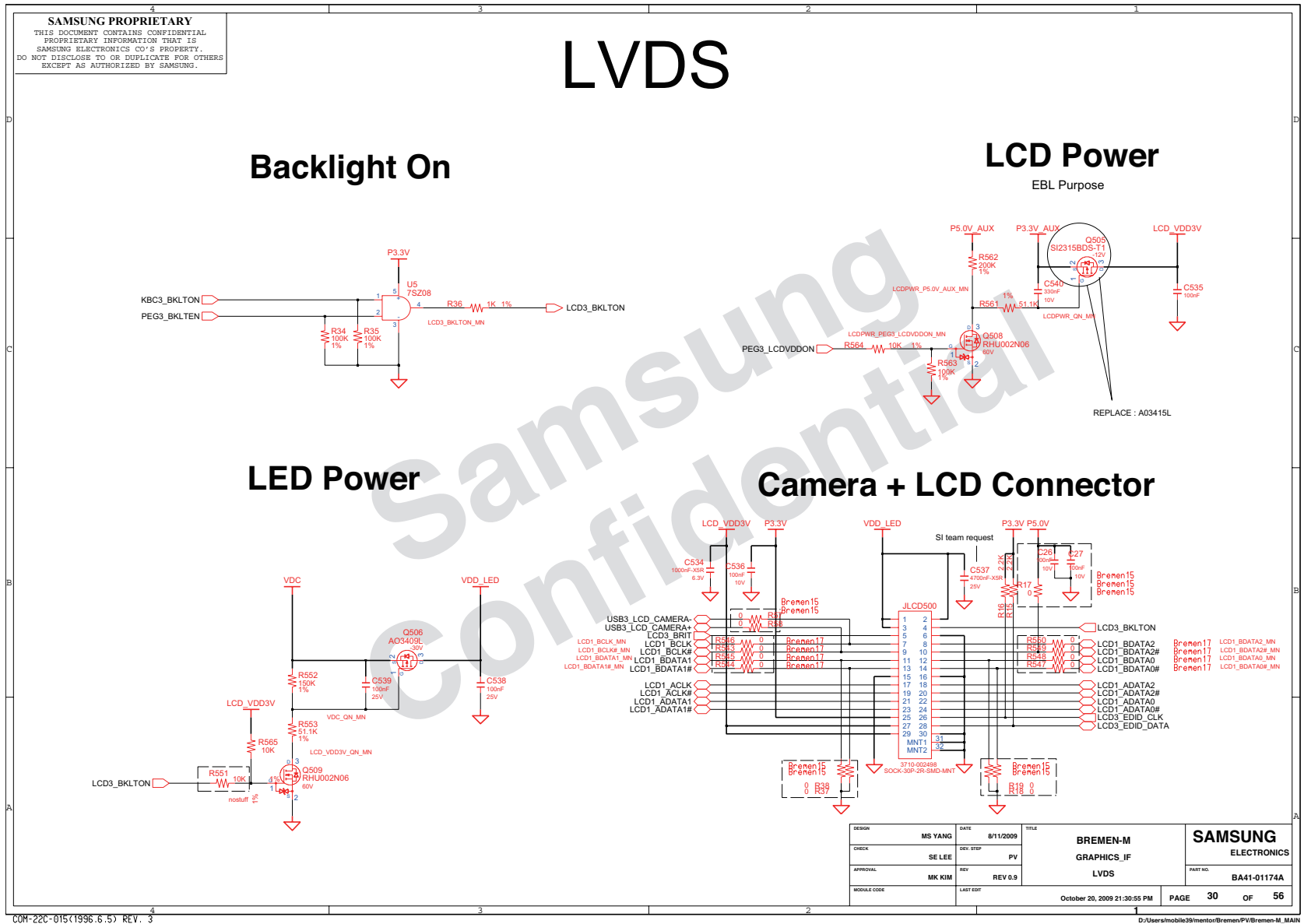
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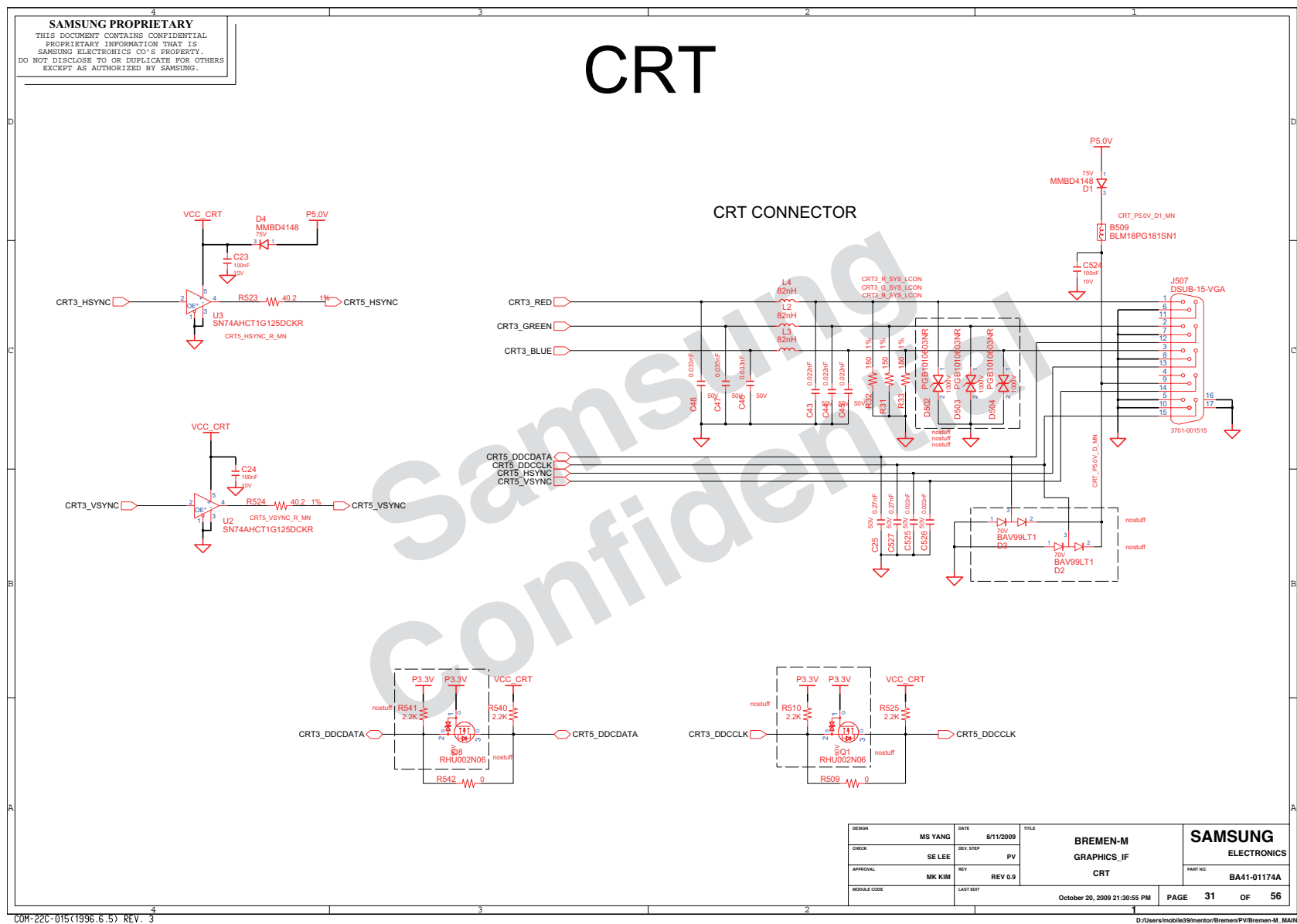
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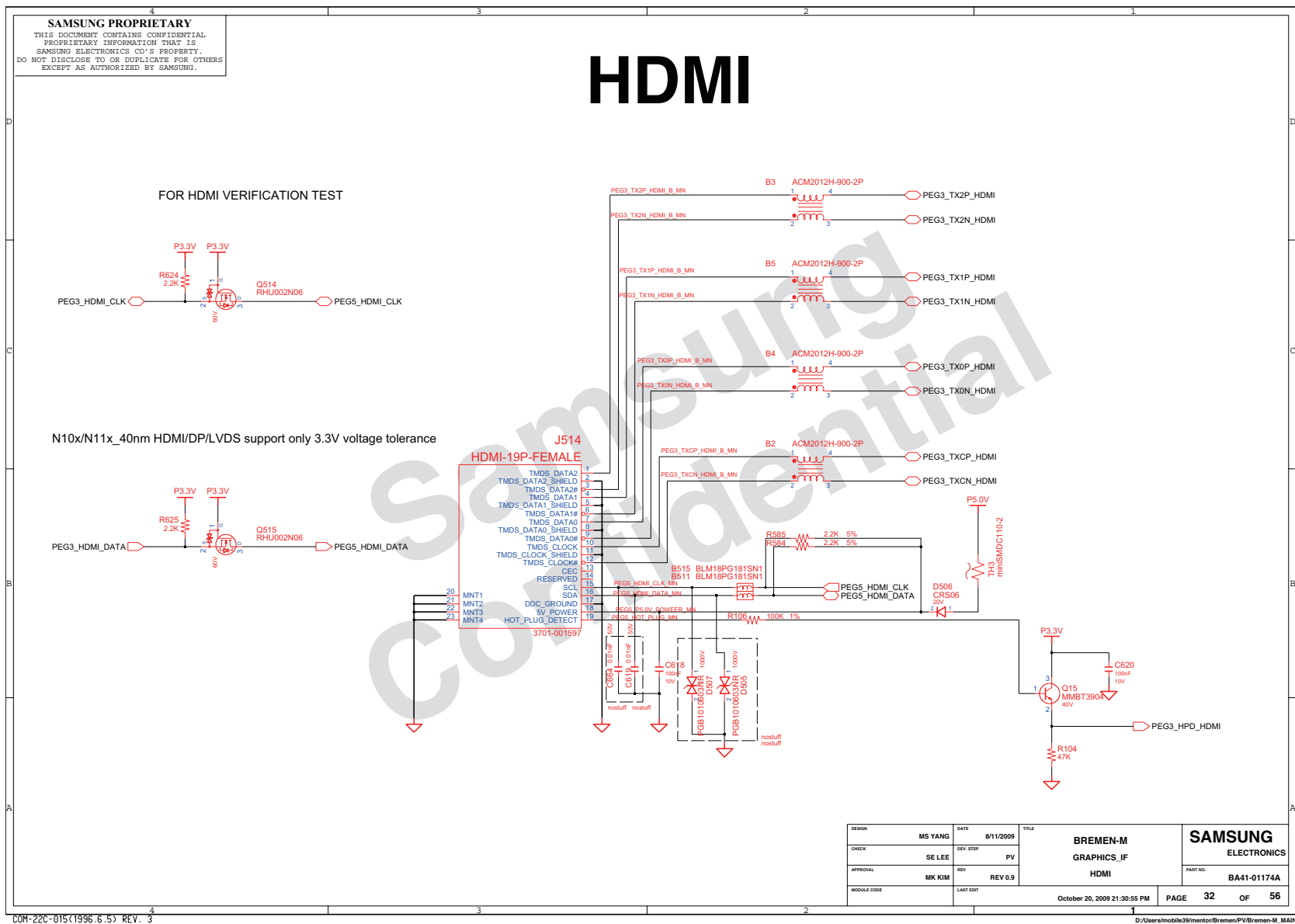
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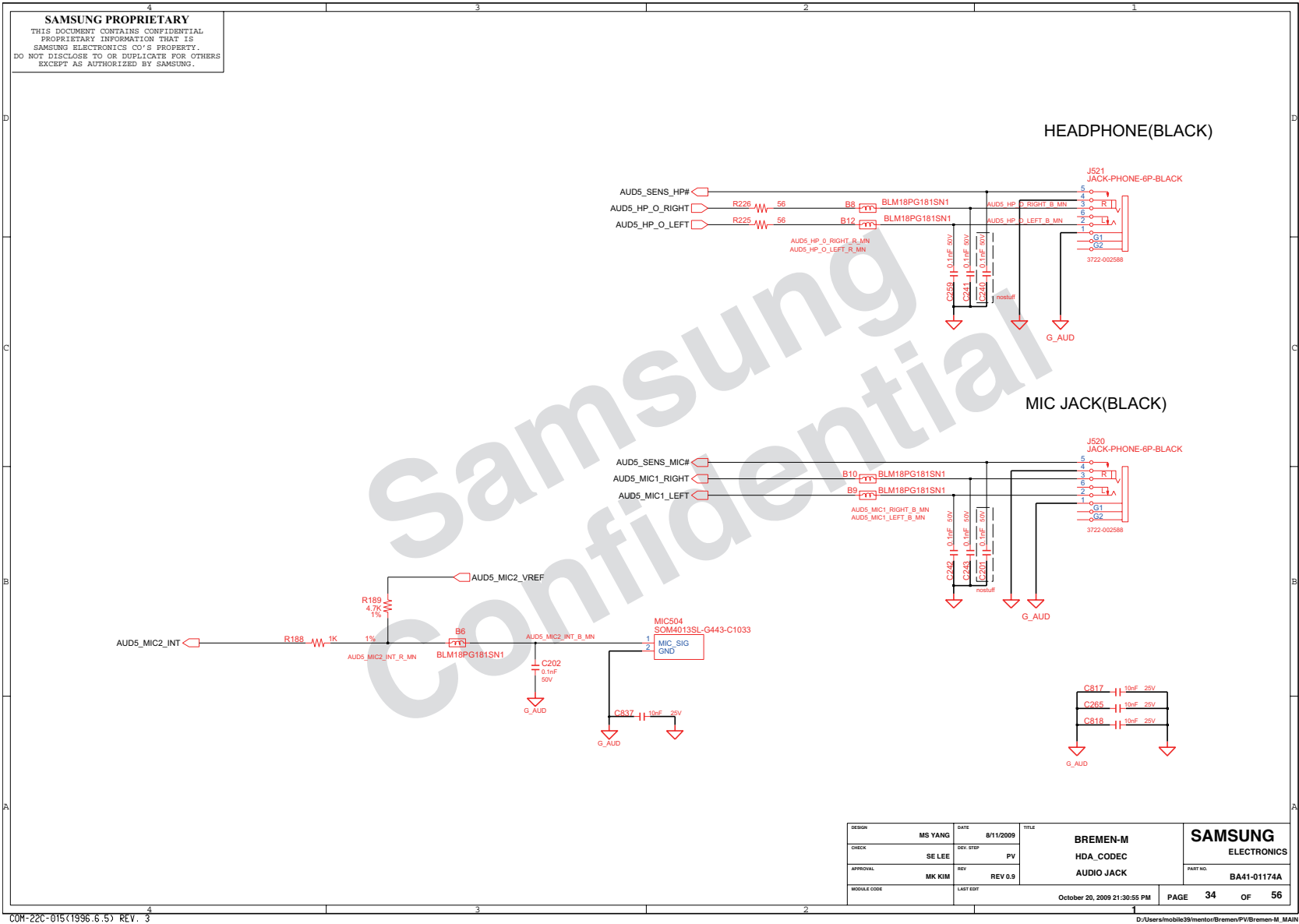
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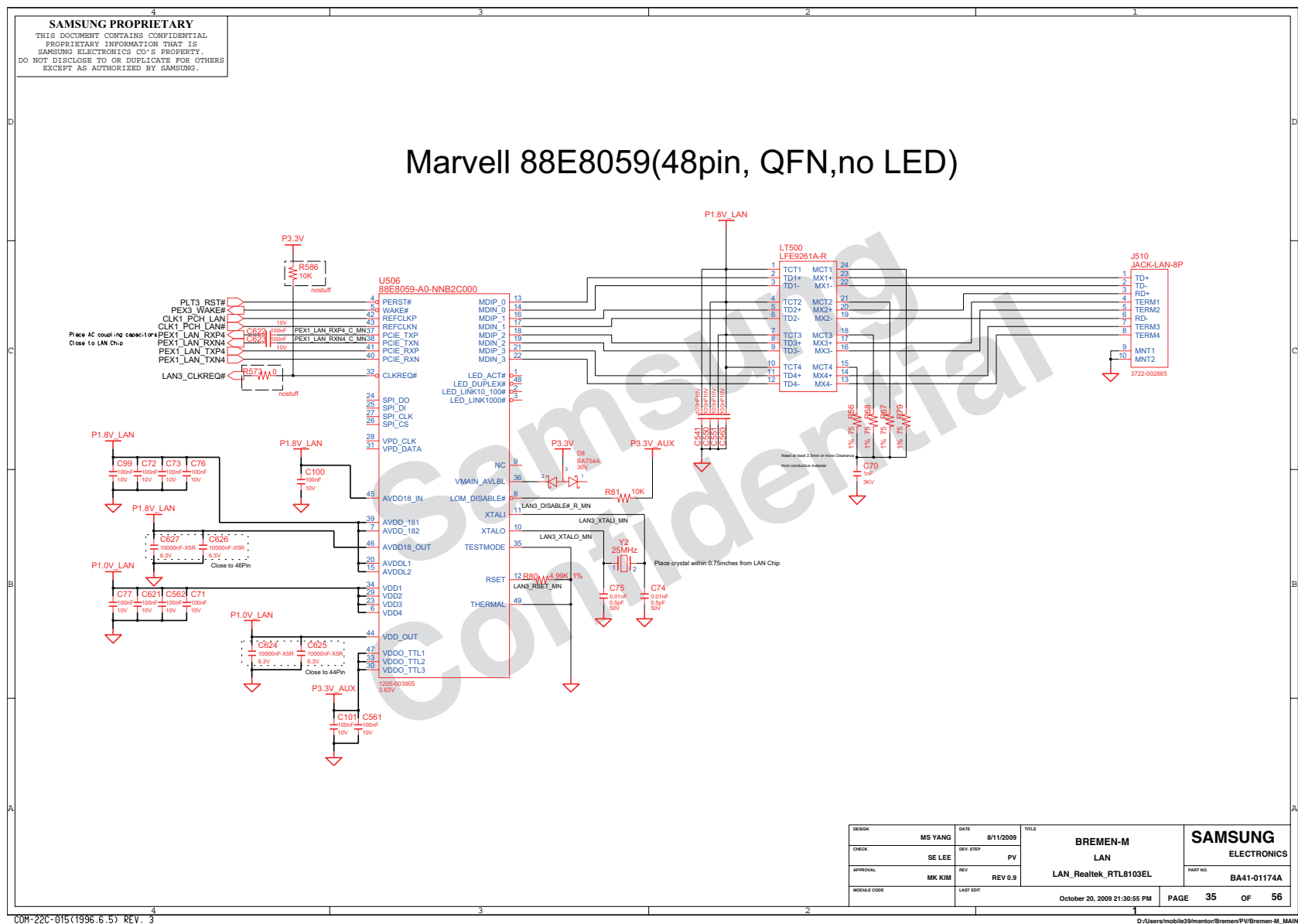
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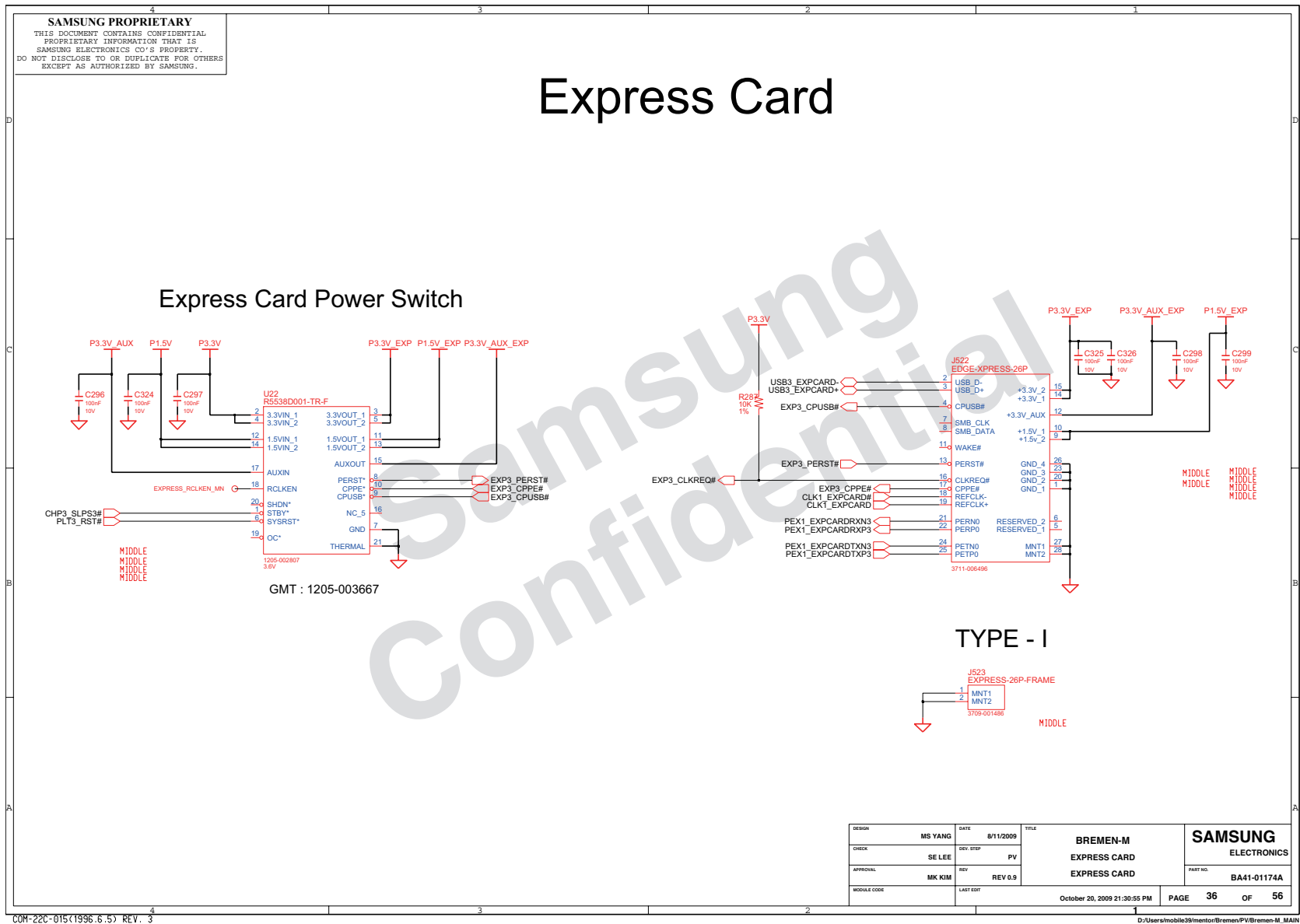
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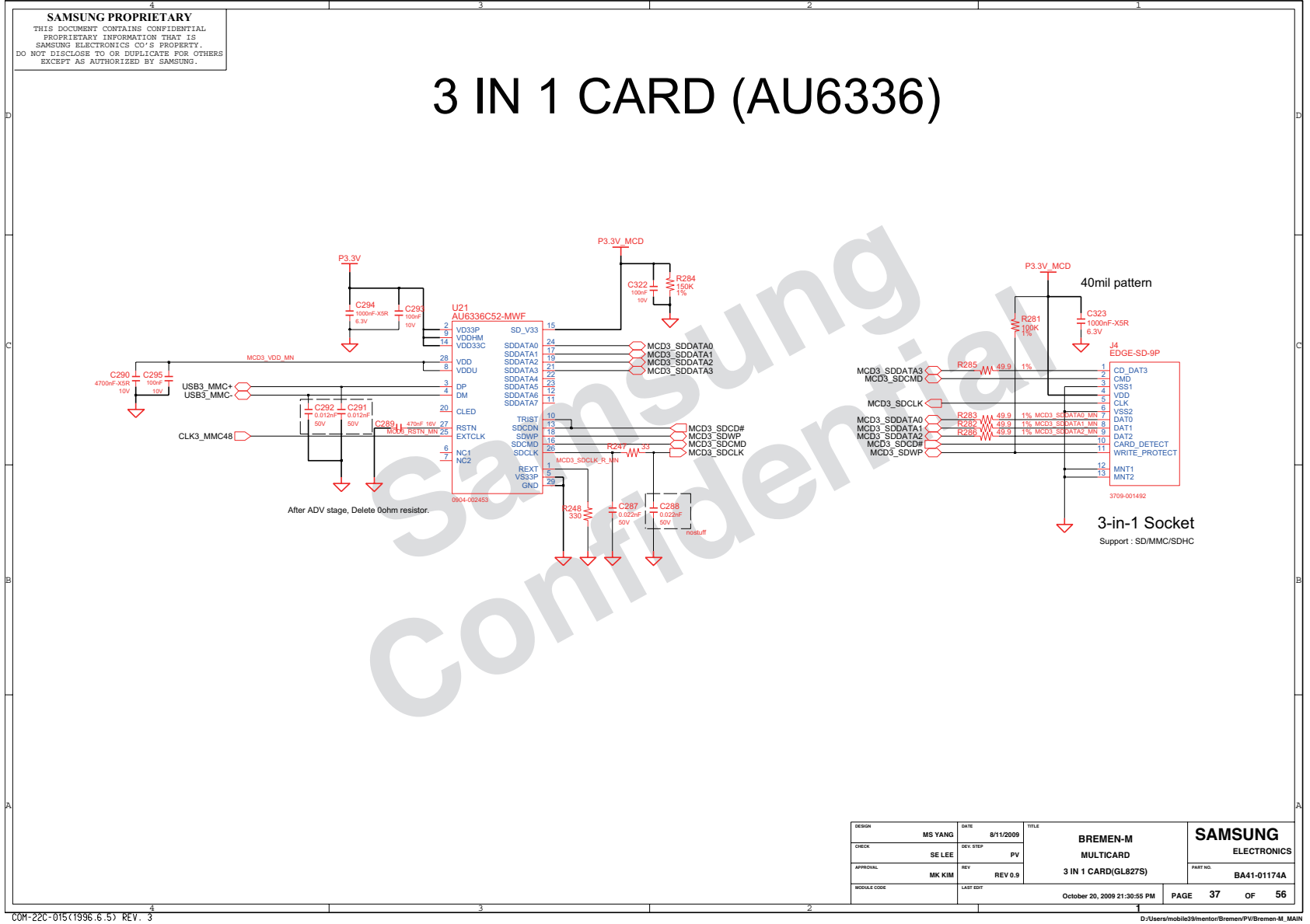
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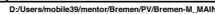


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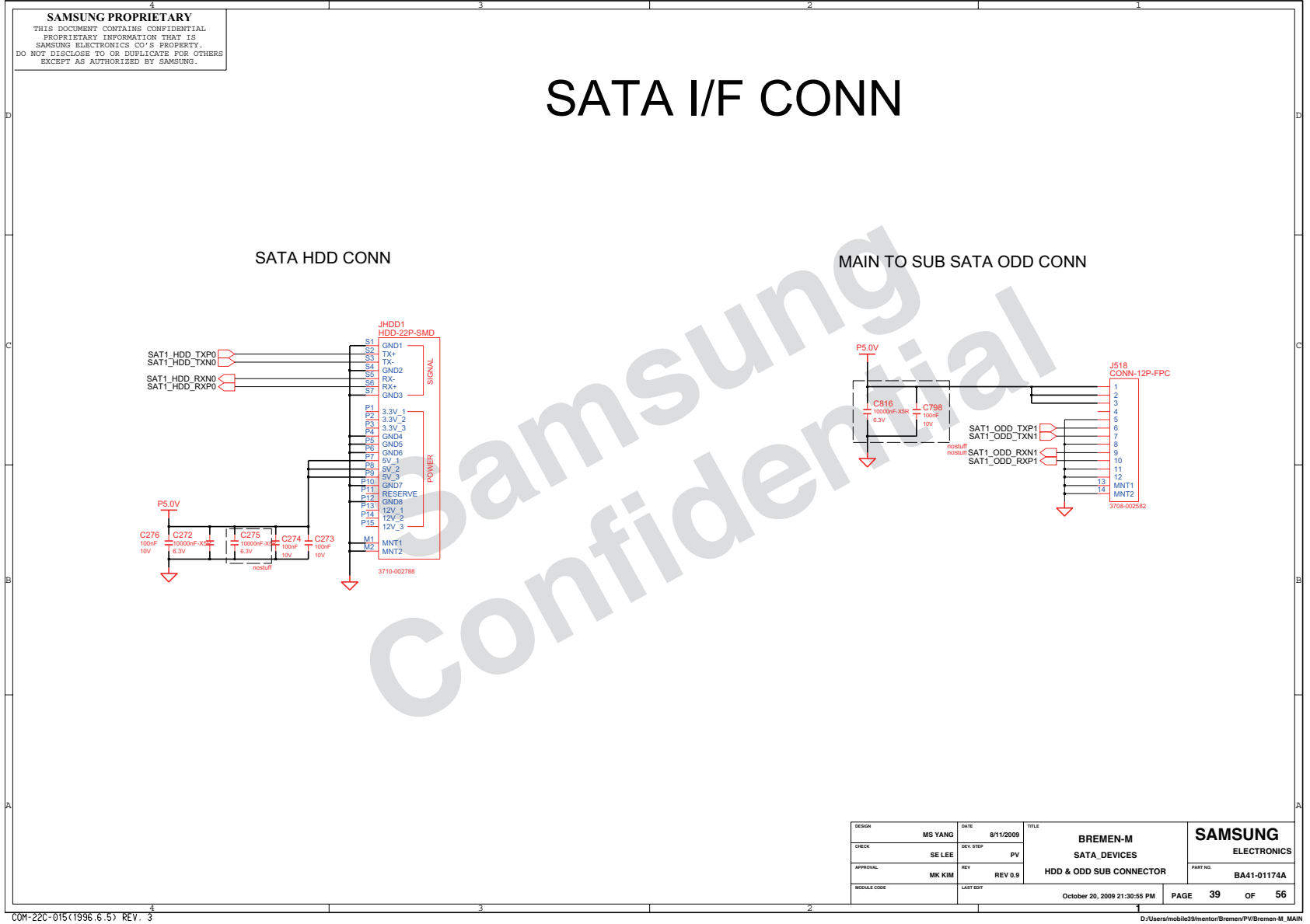


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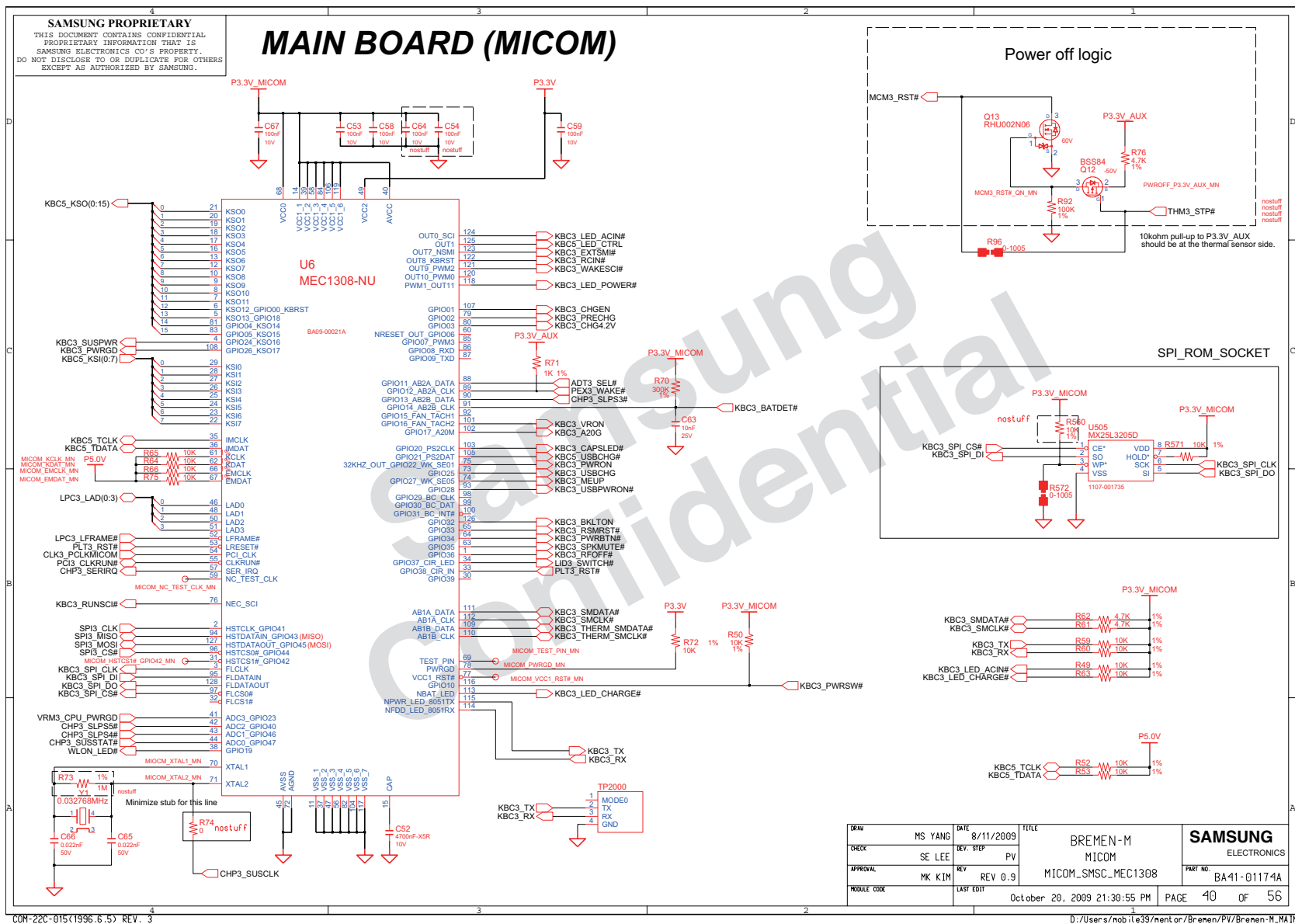




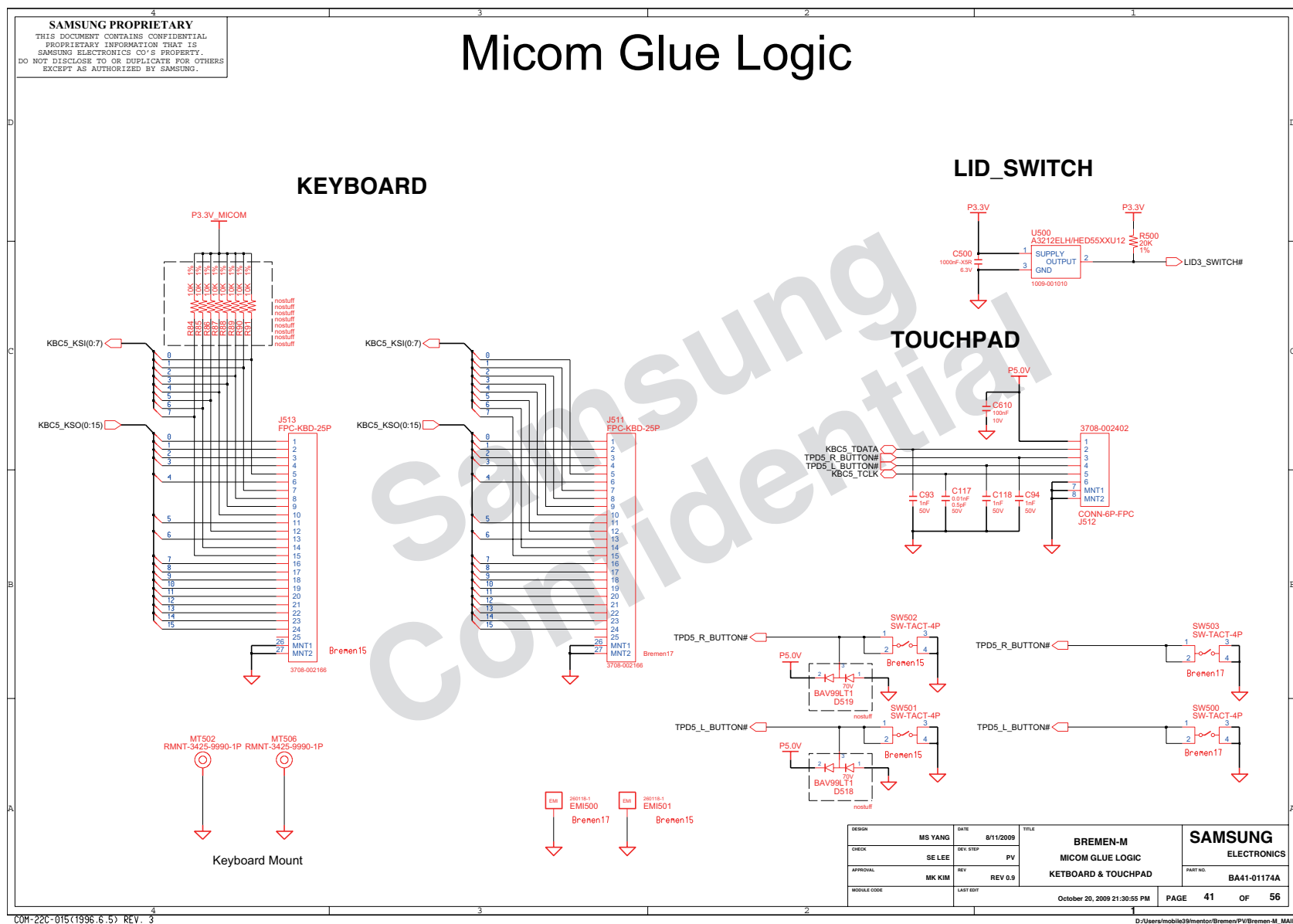
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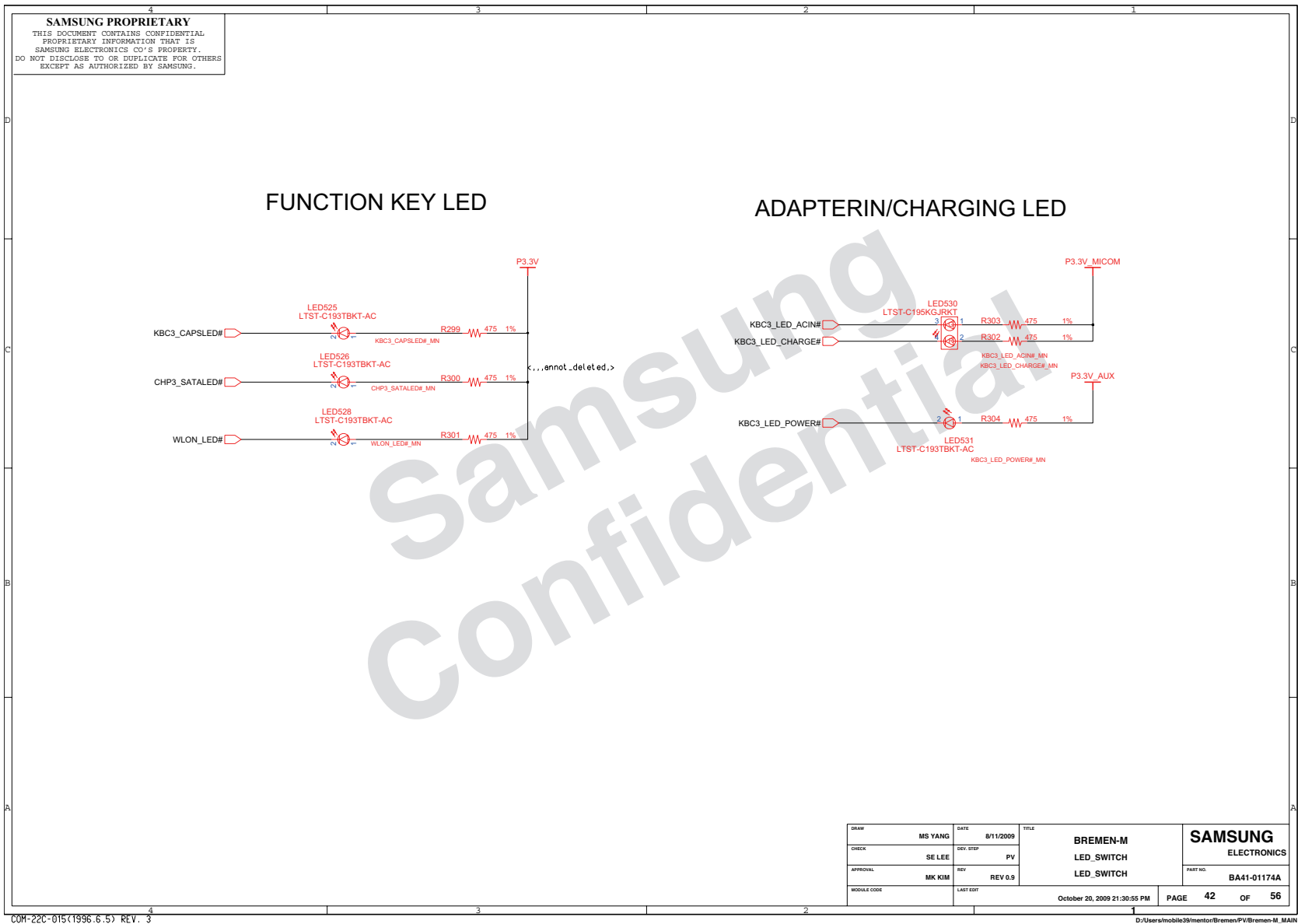
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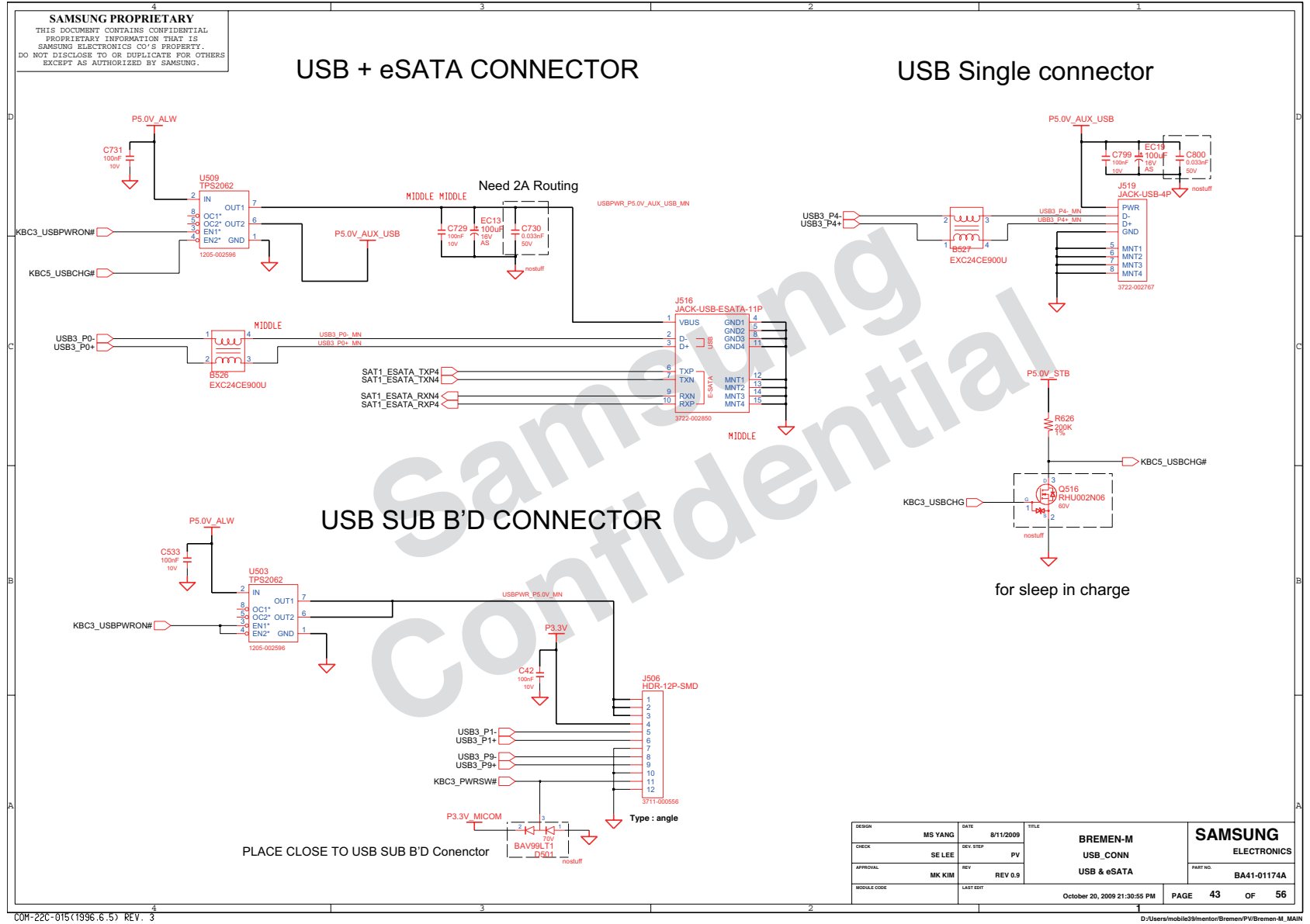
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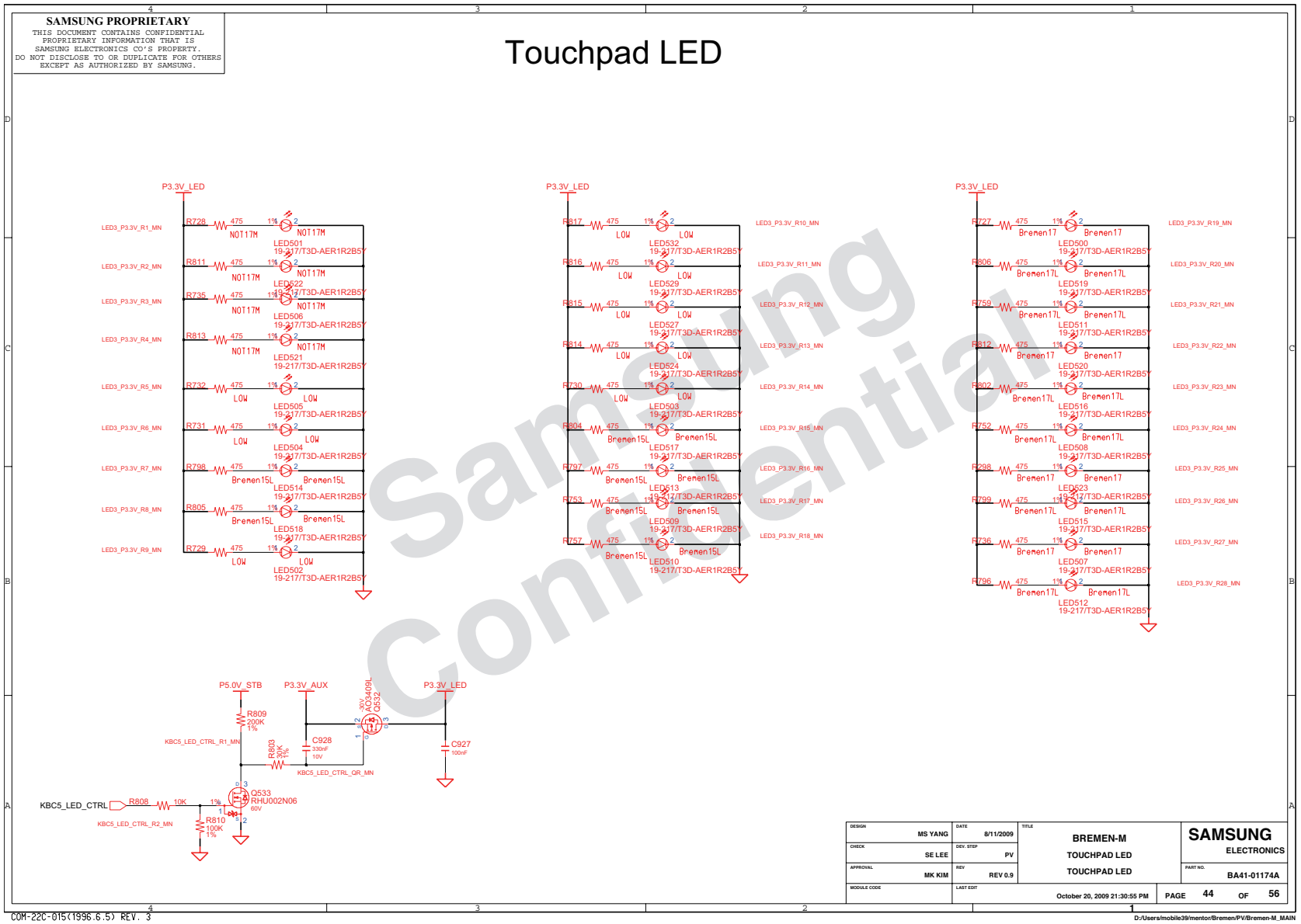
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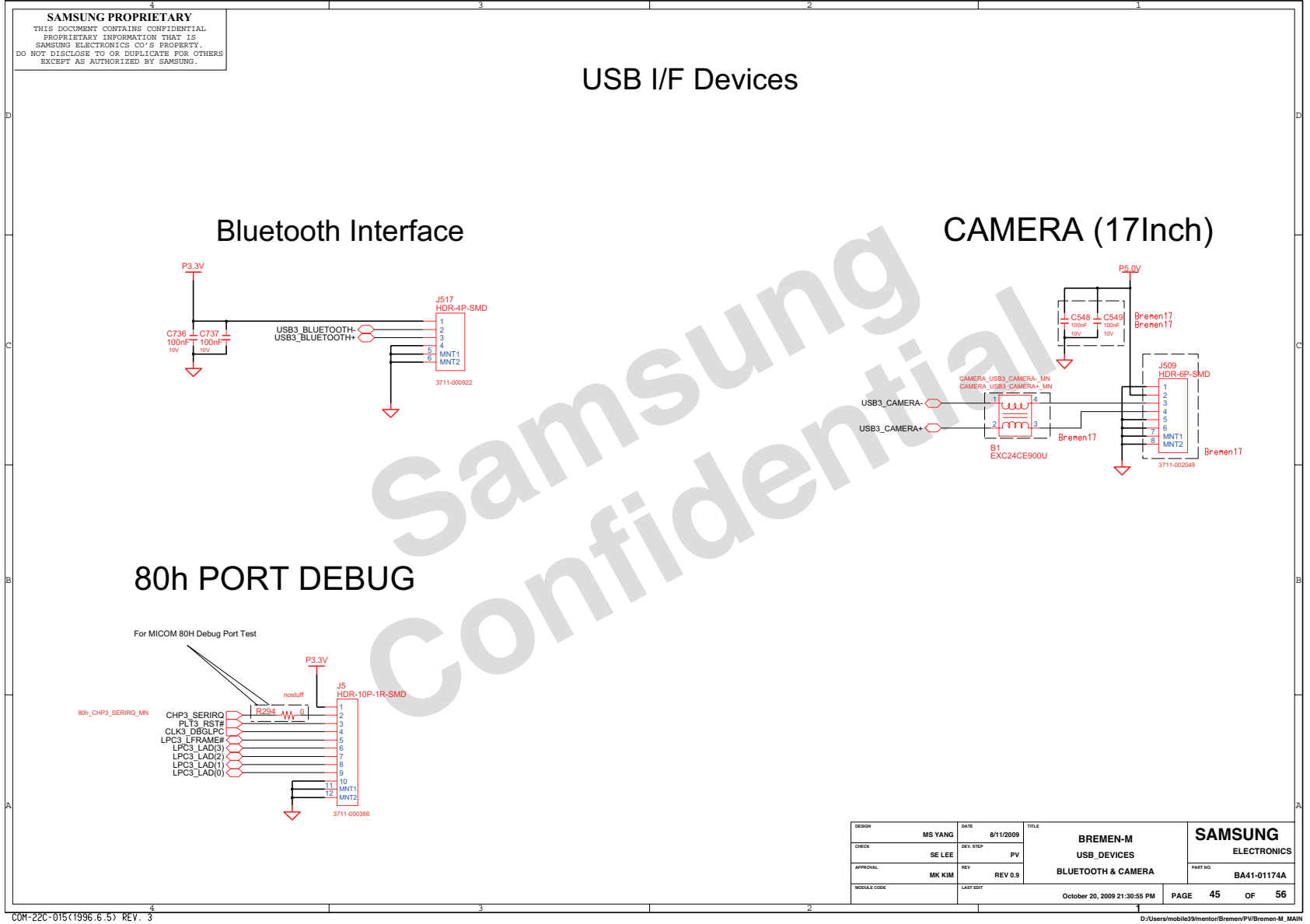
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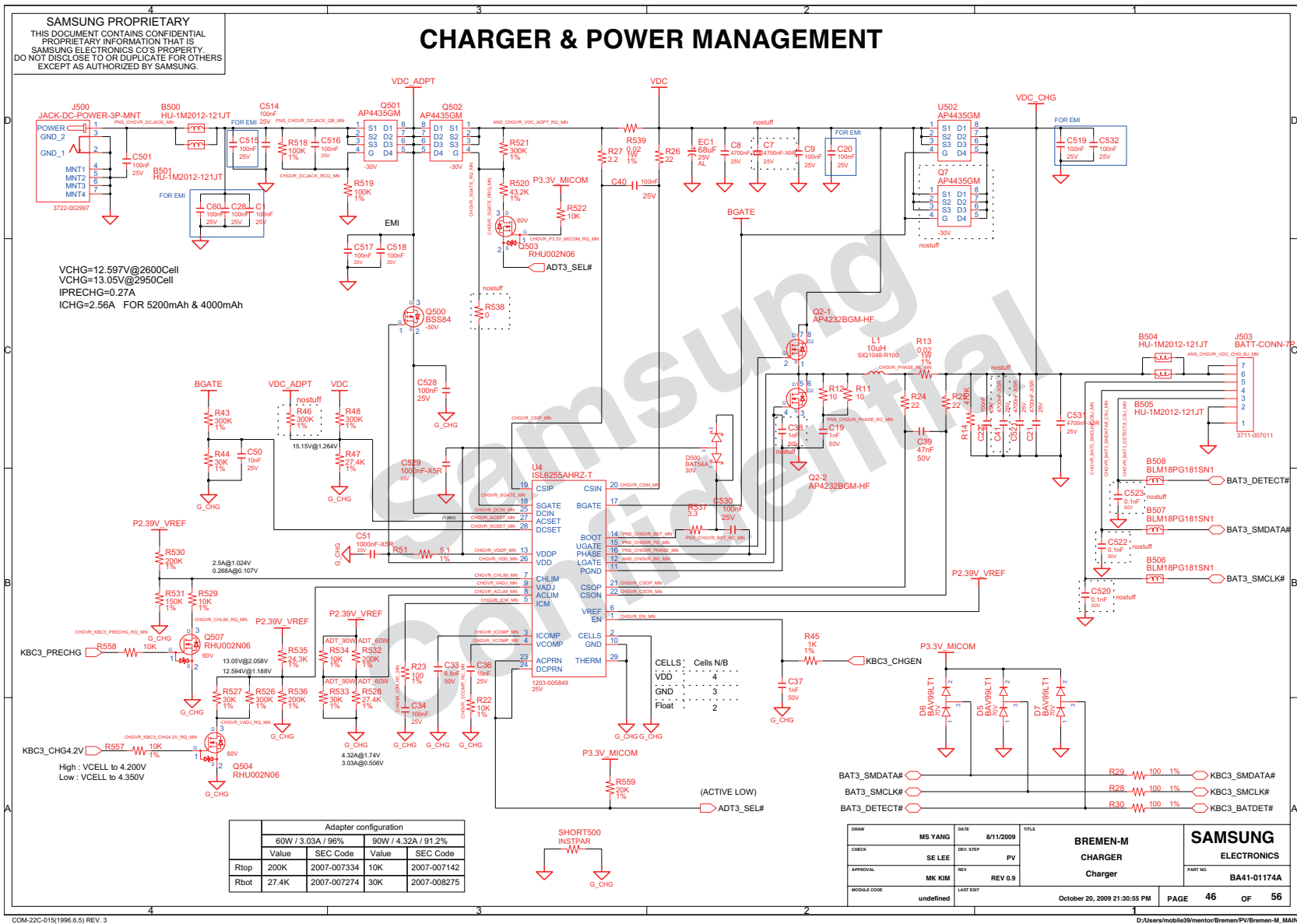
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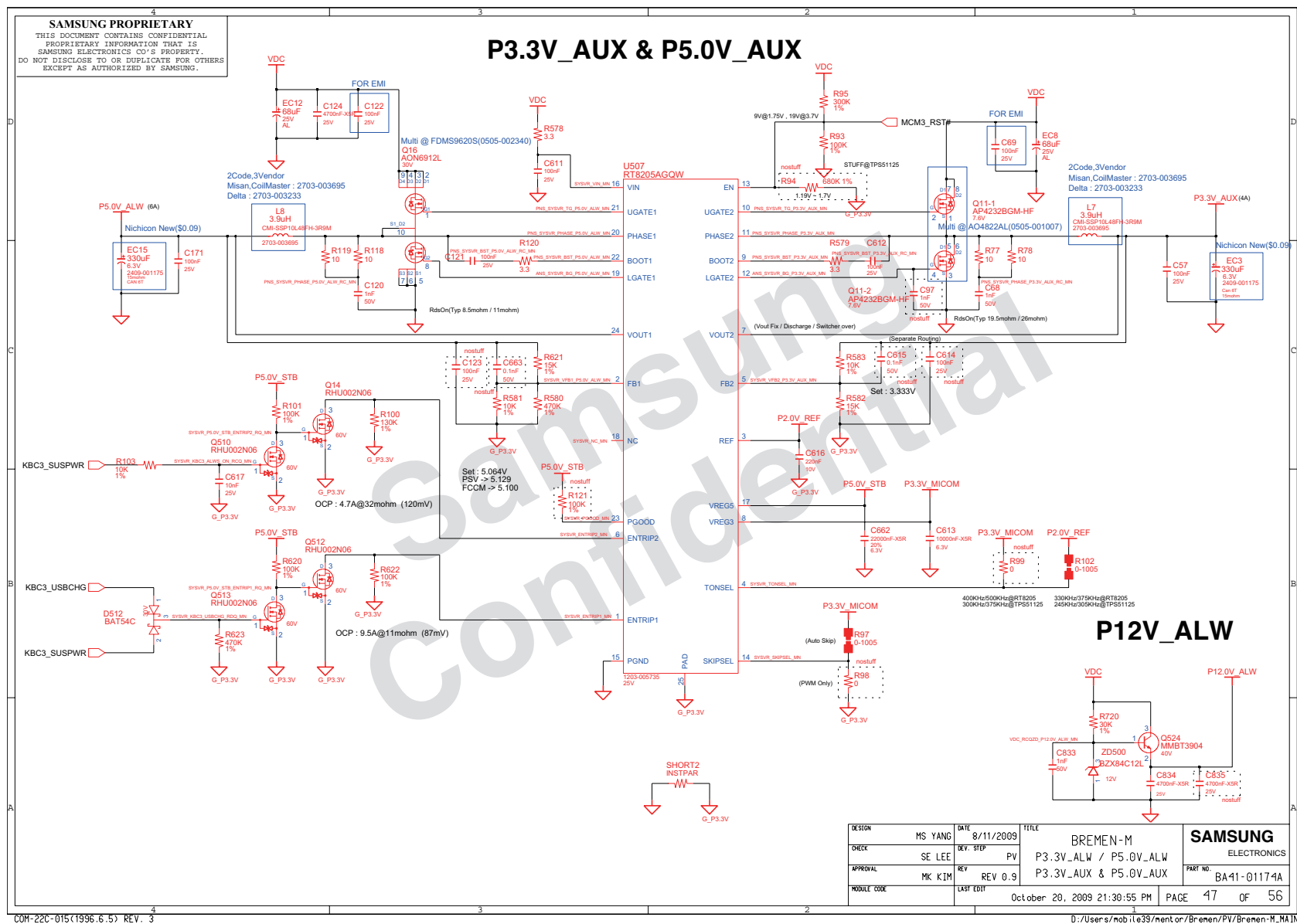
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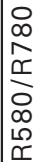
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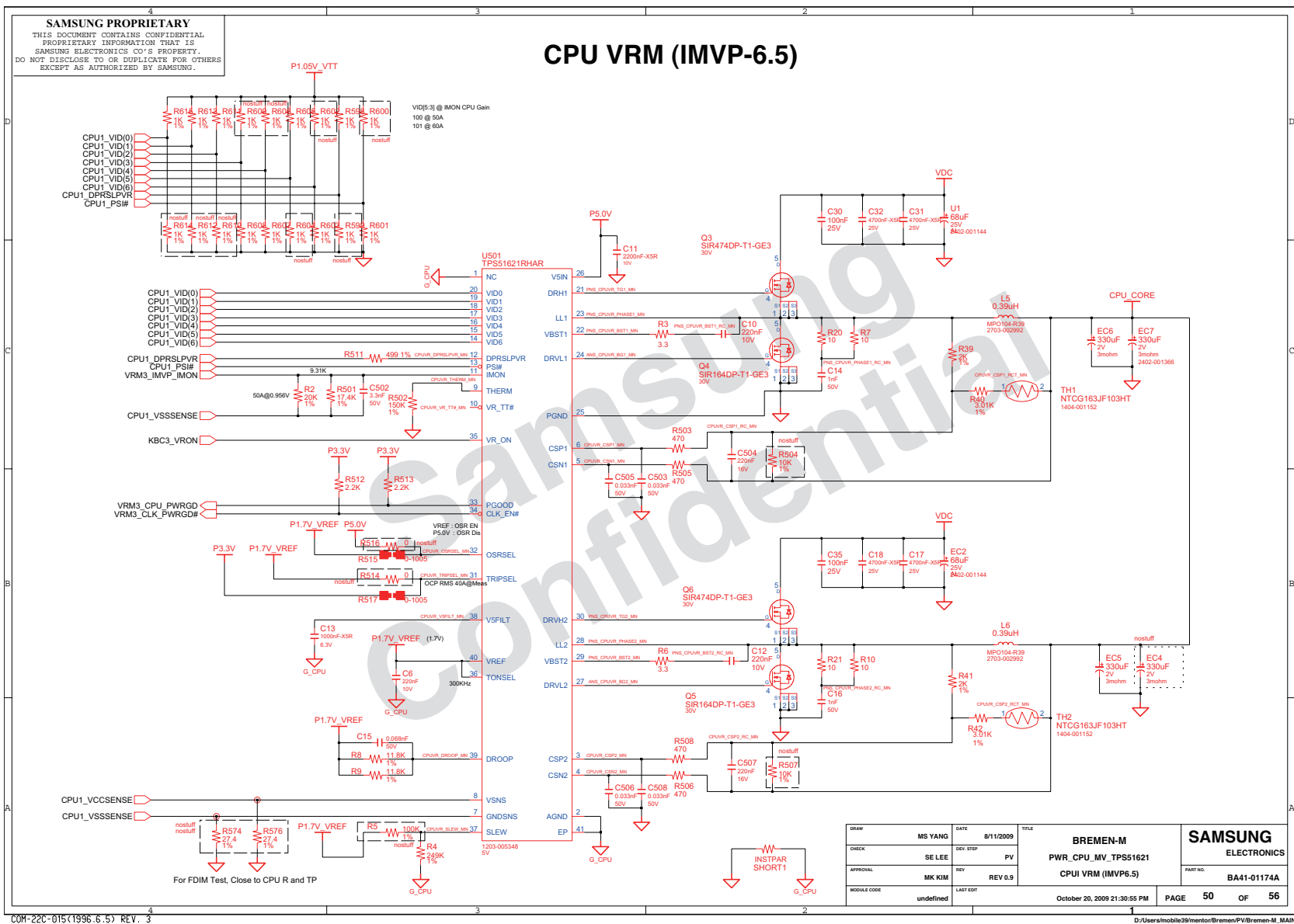
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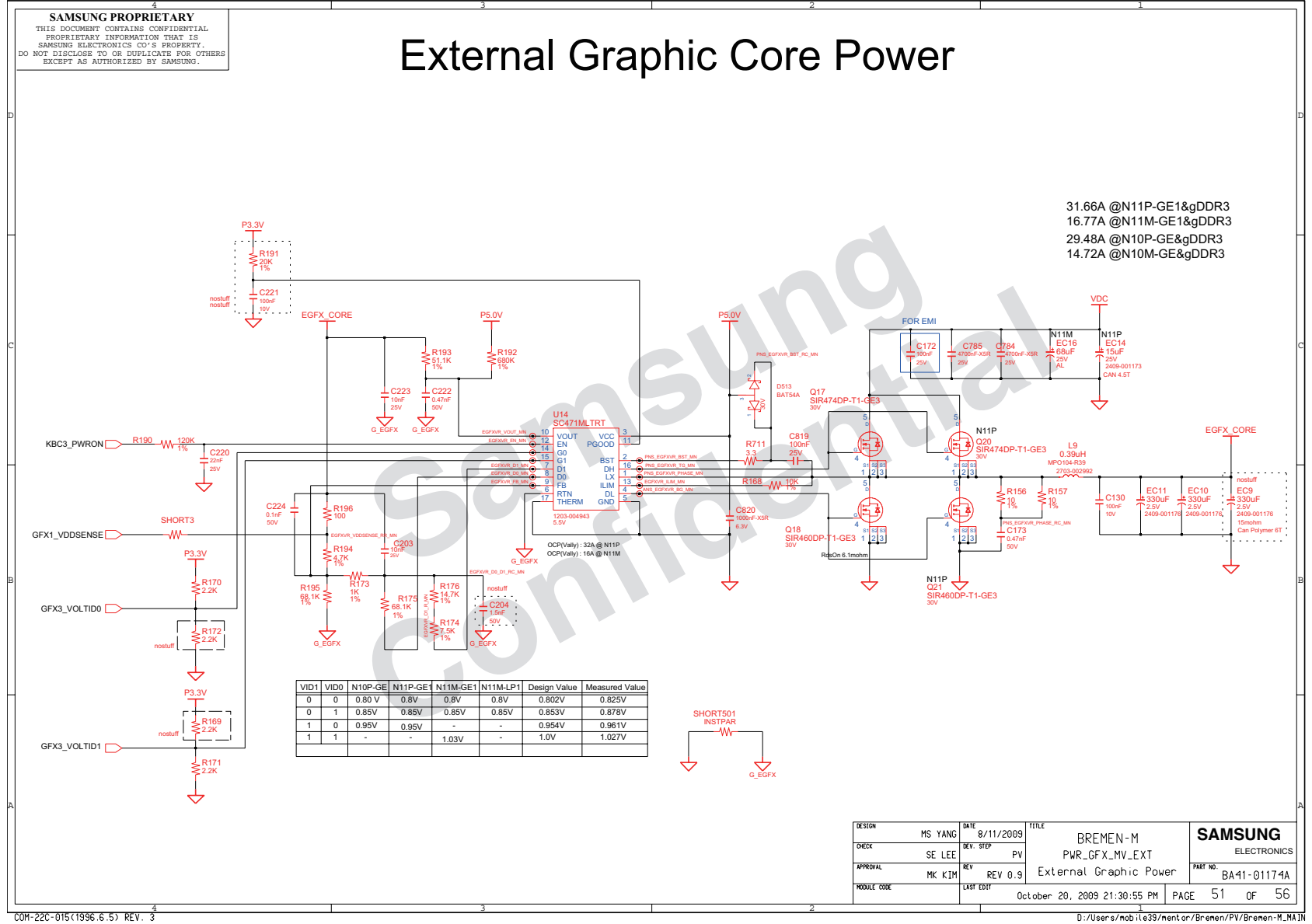




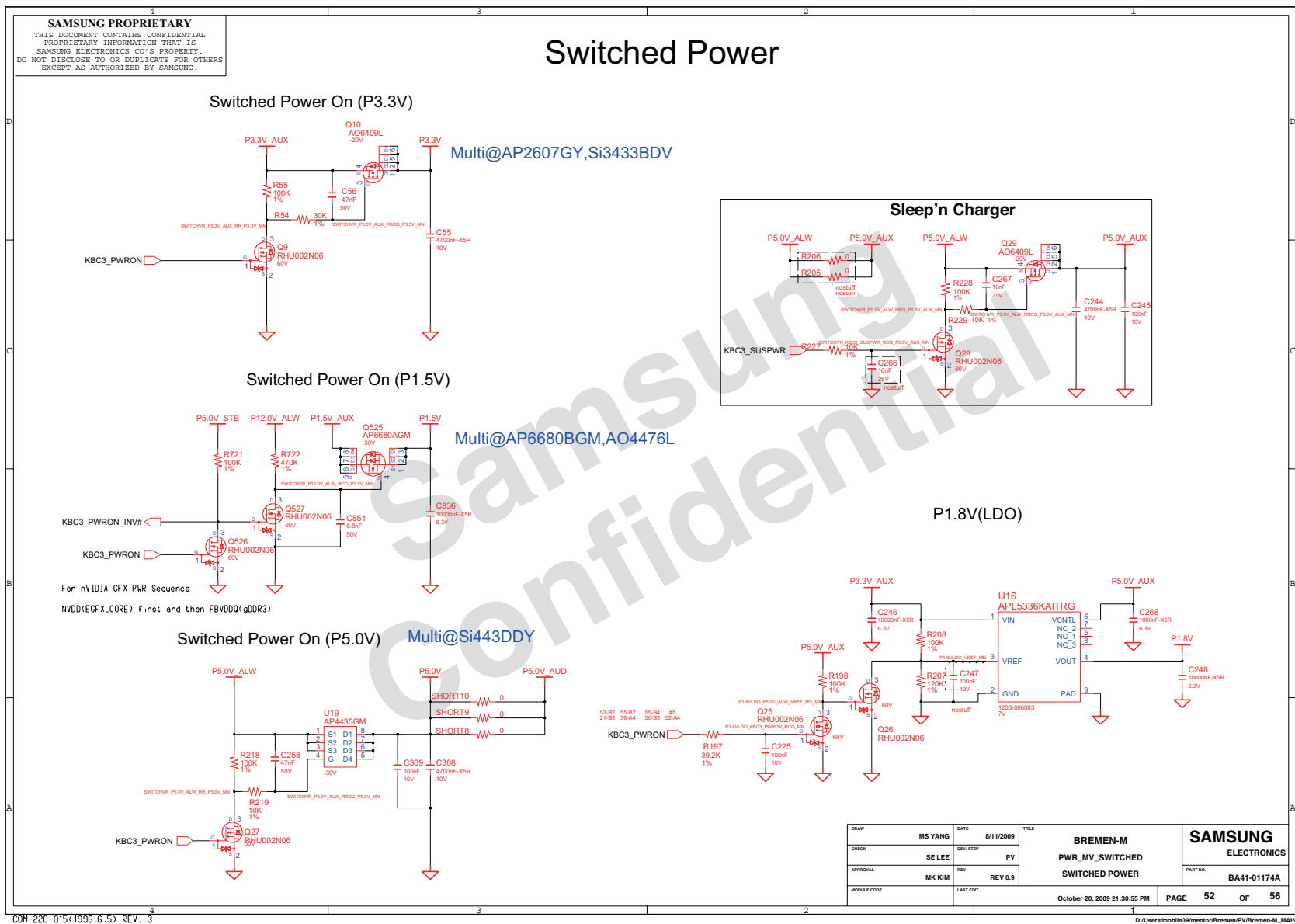
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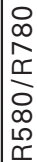


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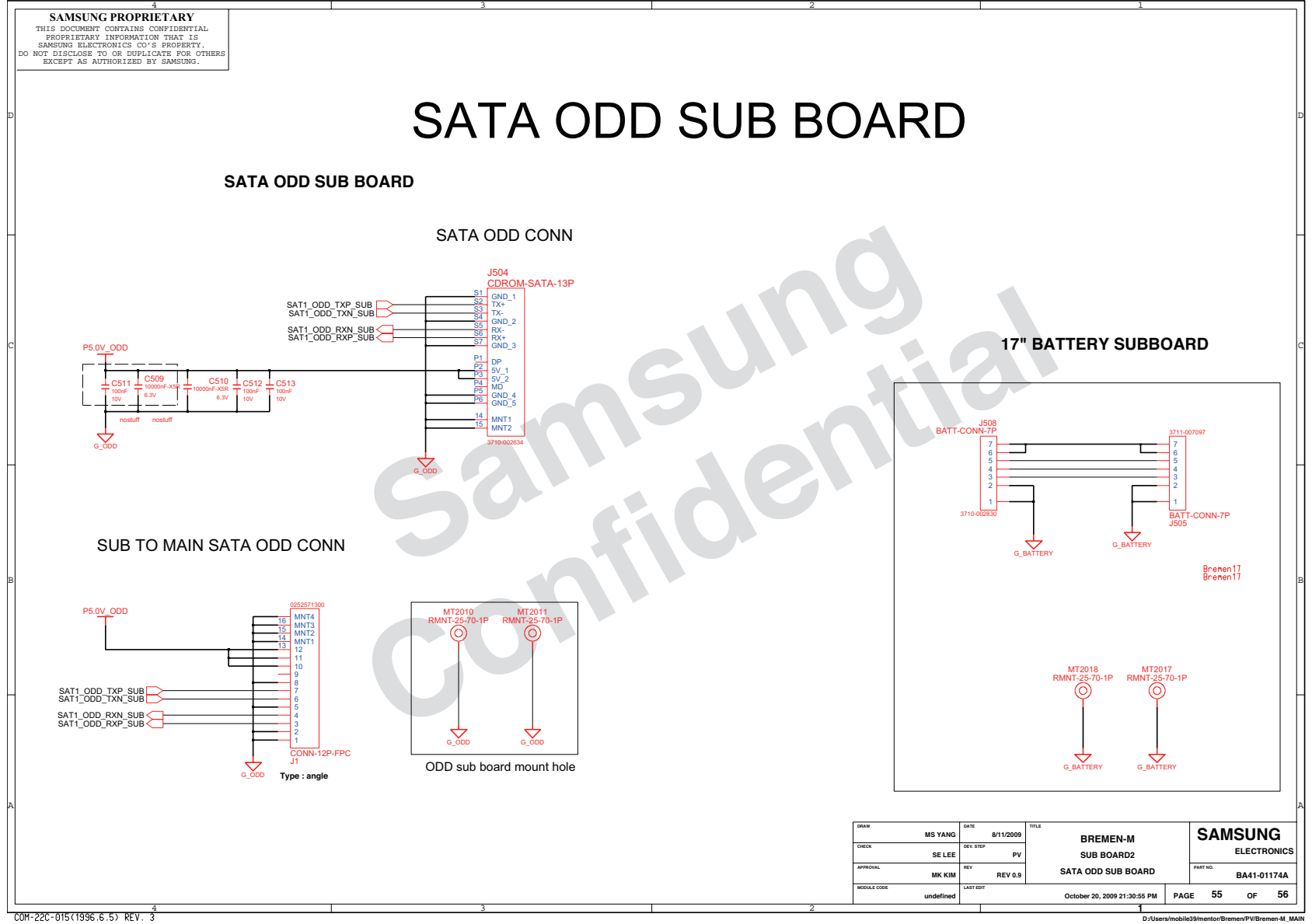
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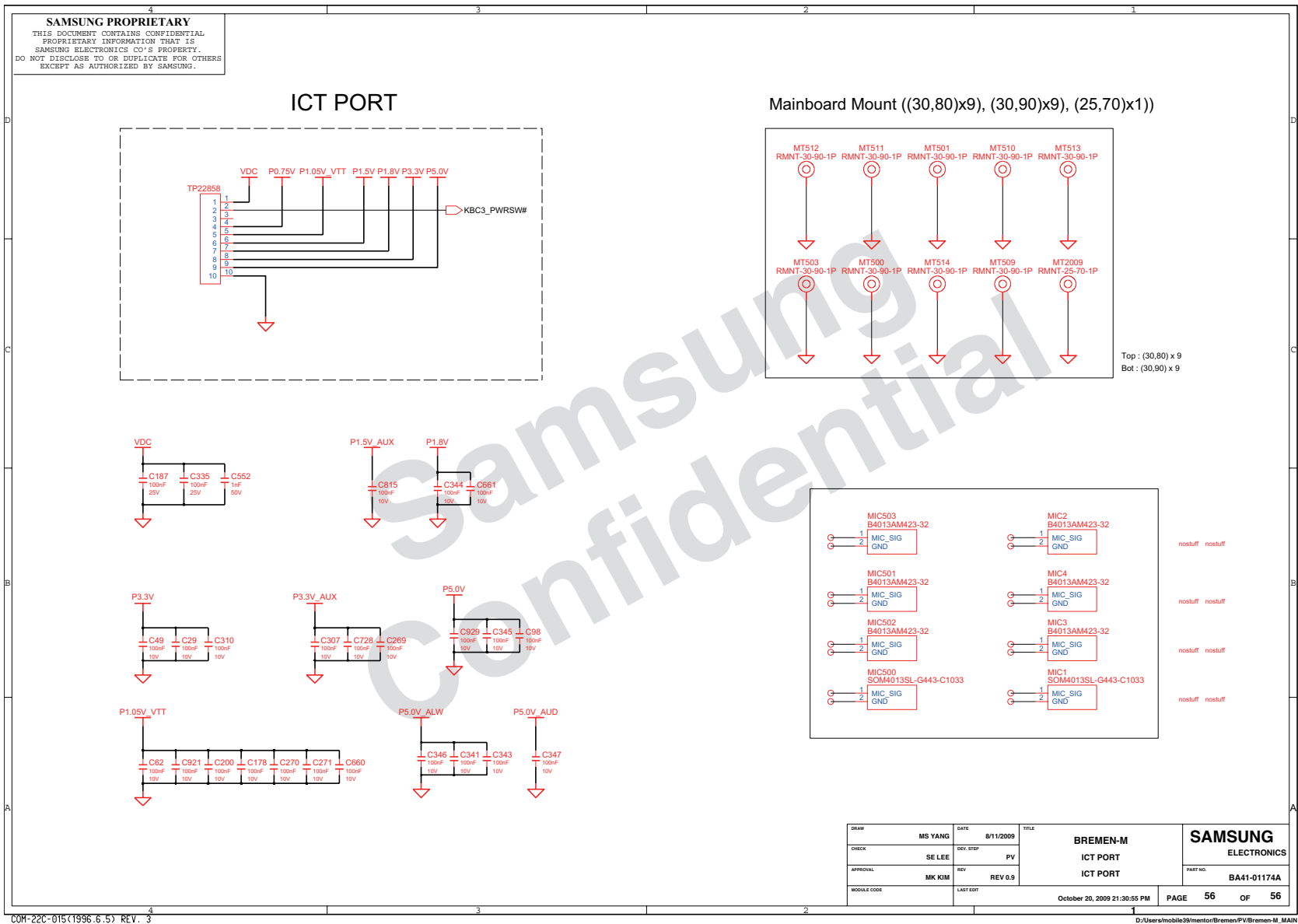




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